

FIG.1

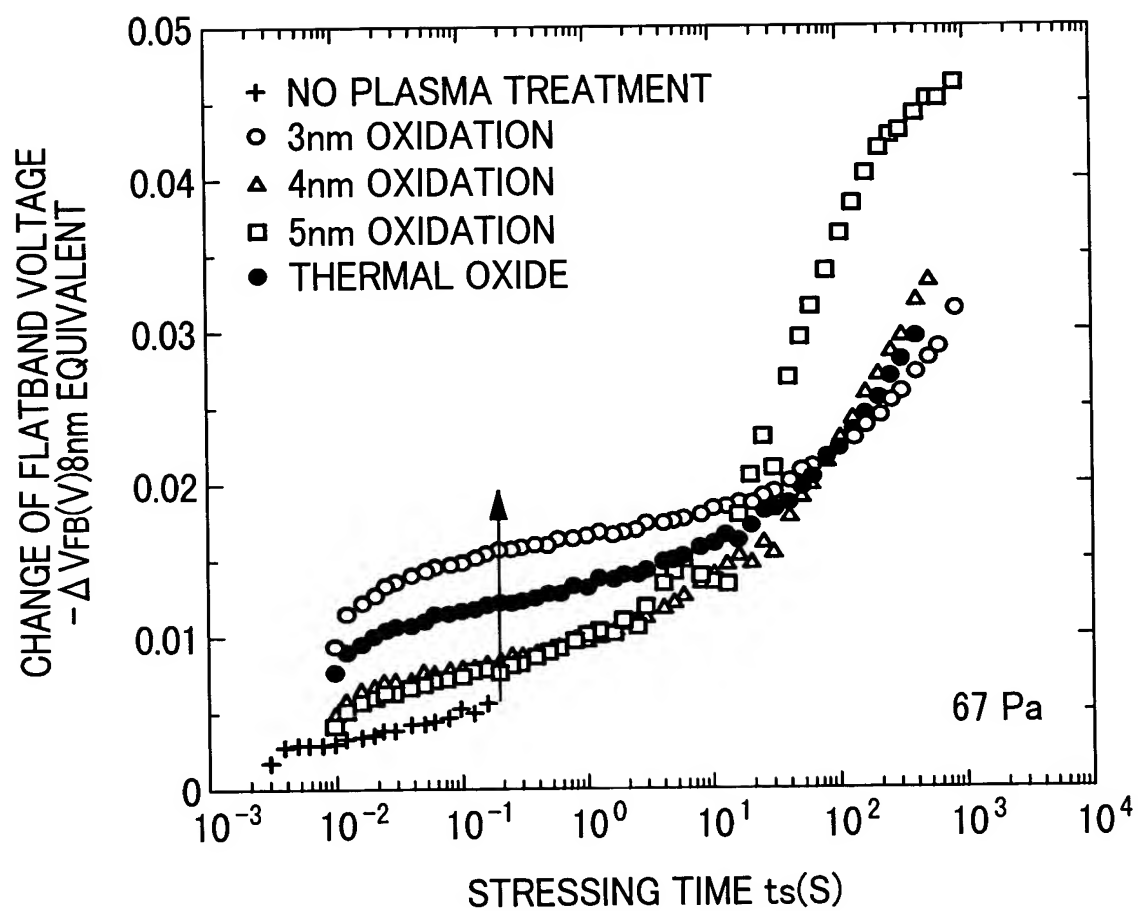


FIG.2

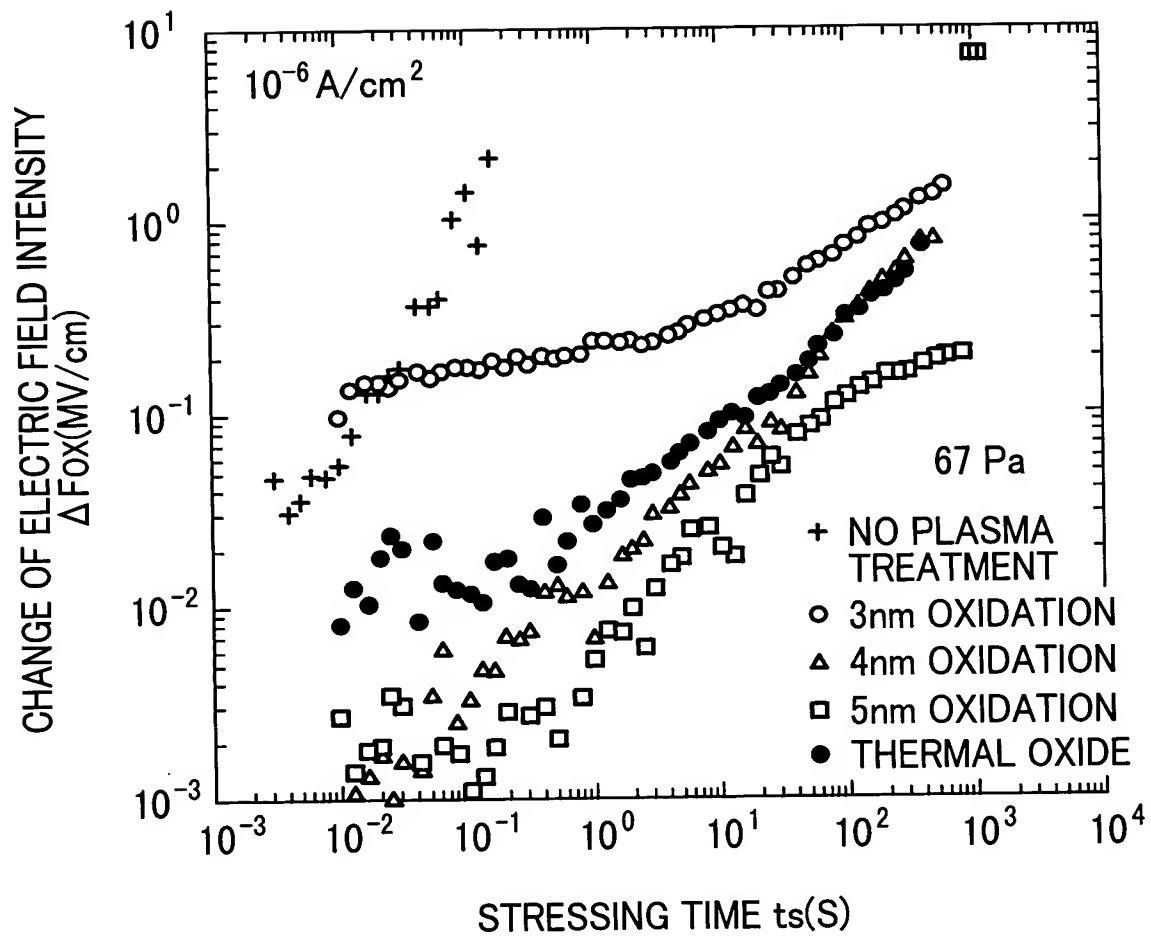
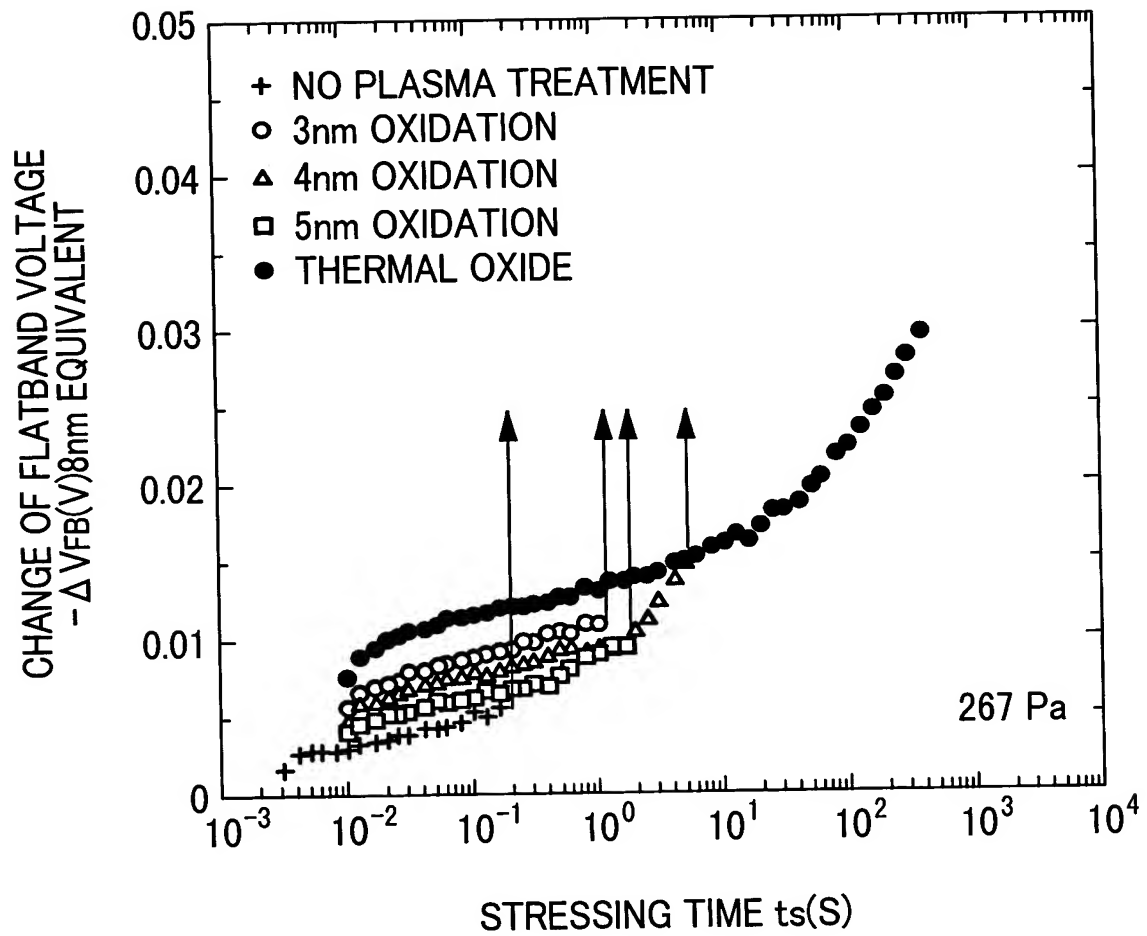


FIG.3



# FIG.4

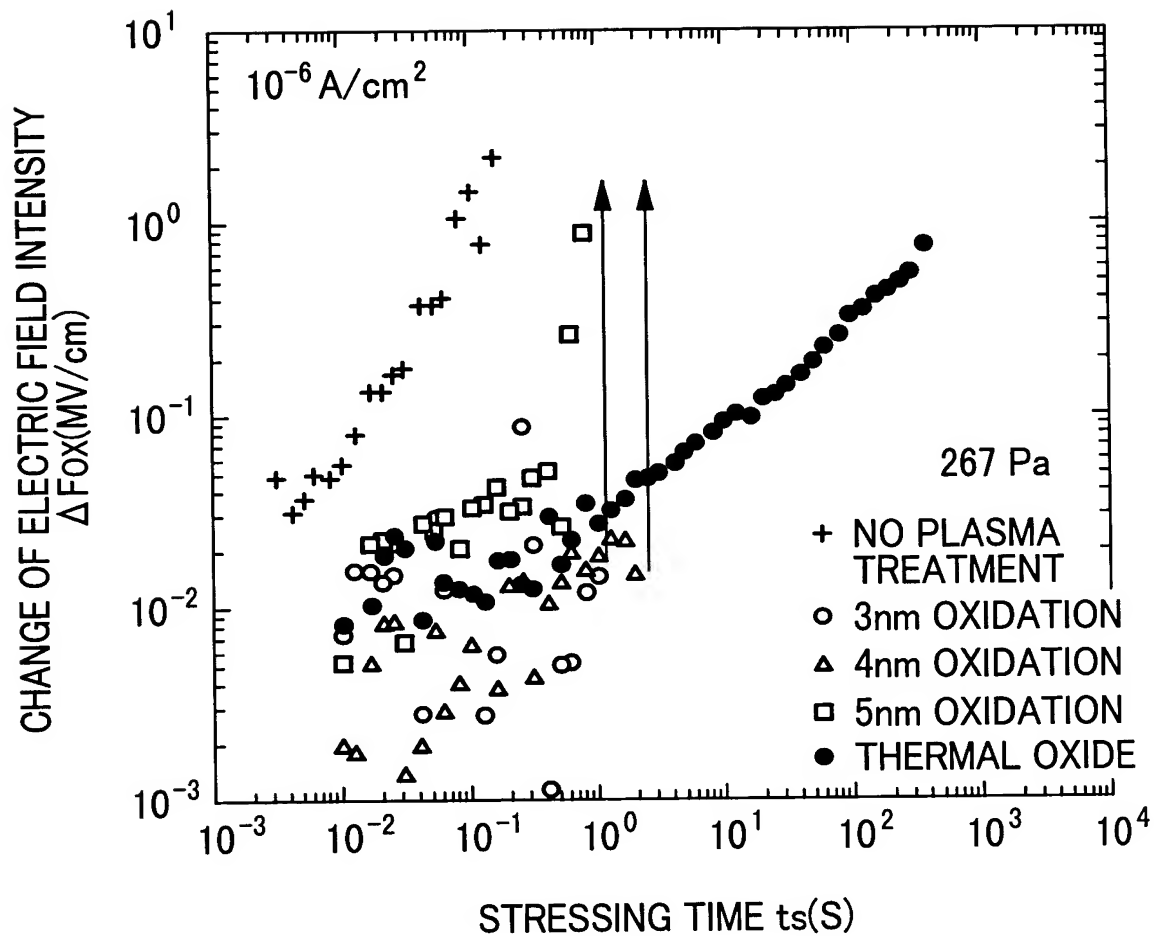


FIG.5

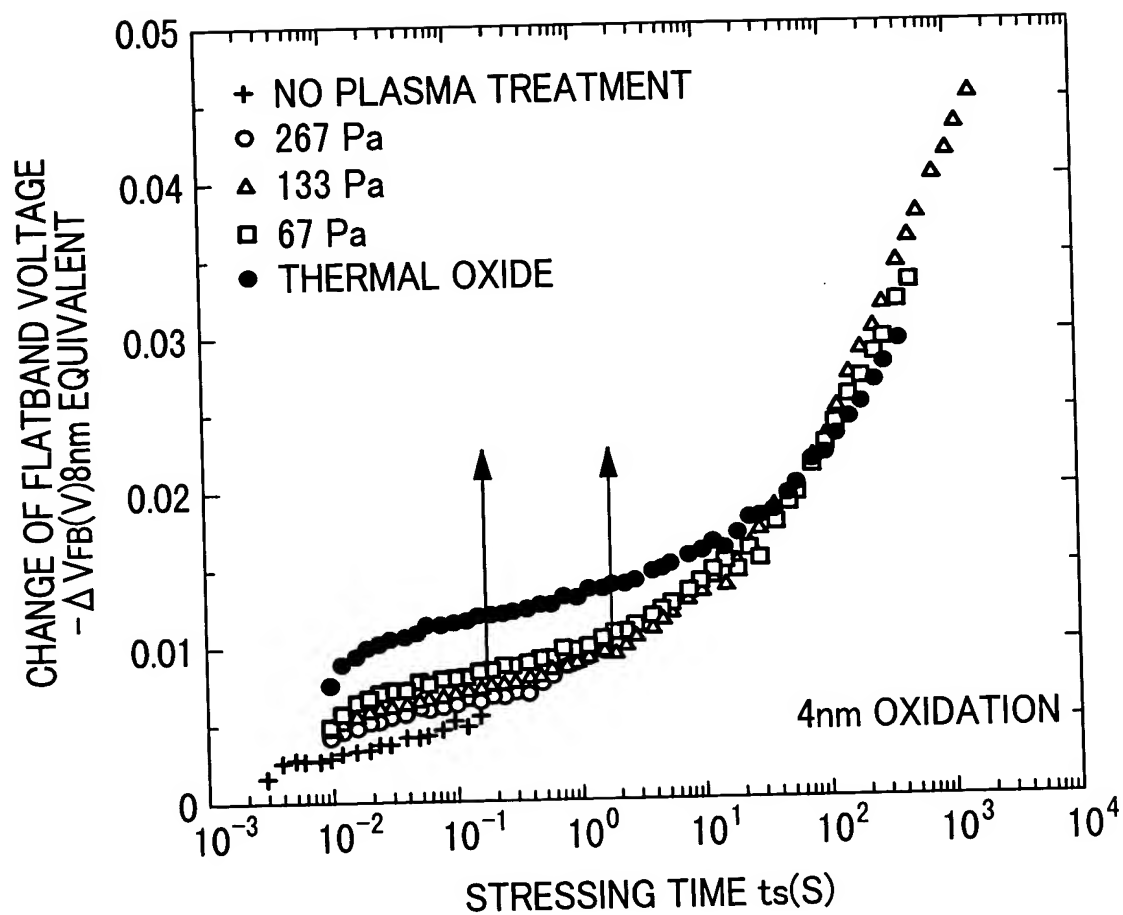
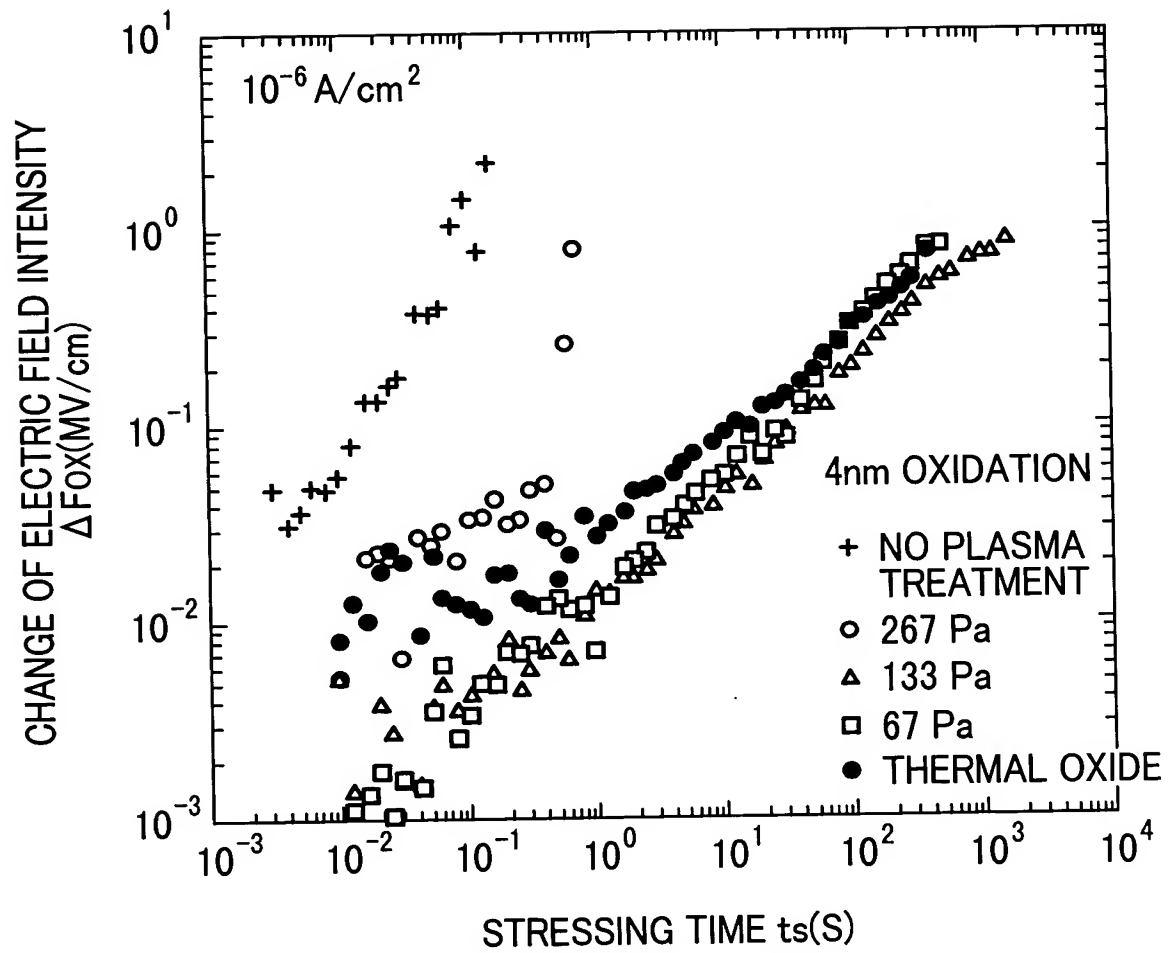
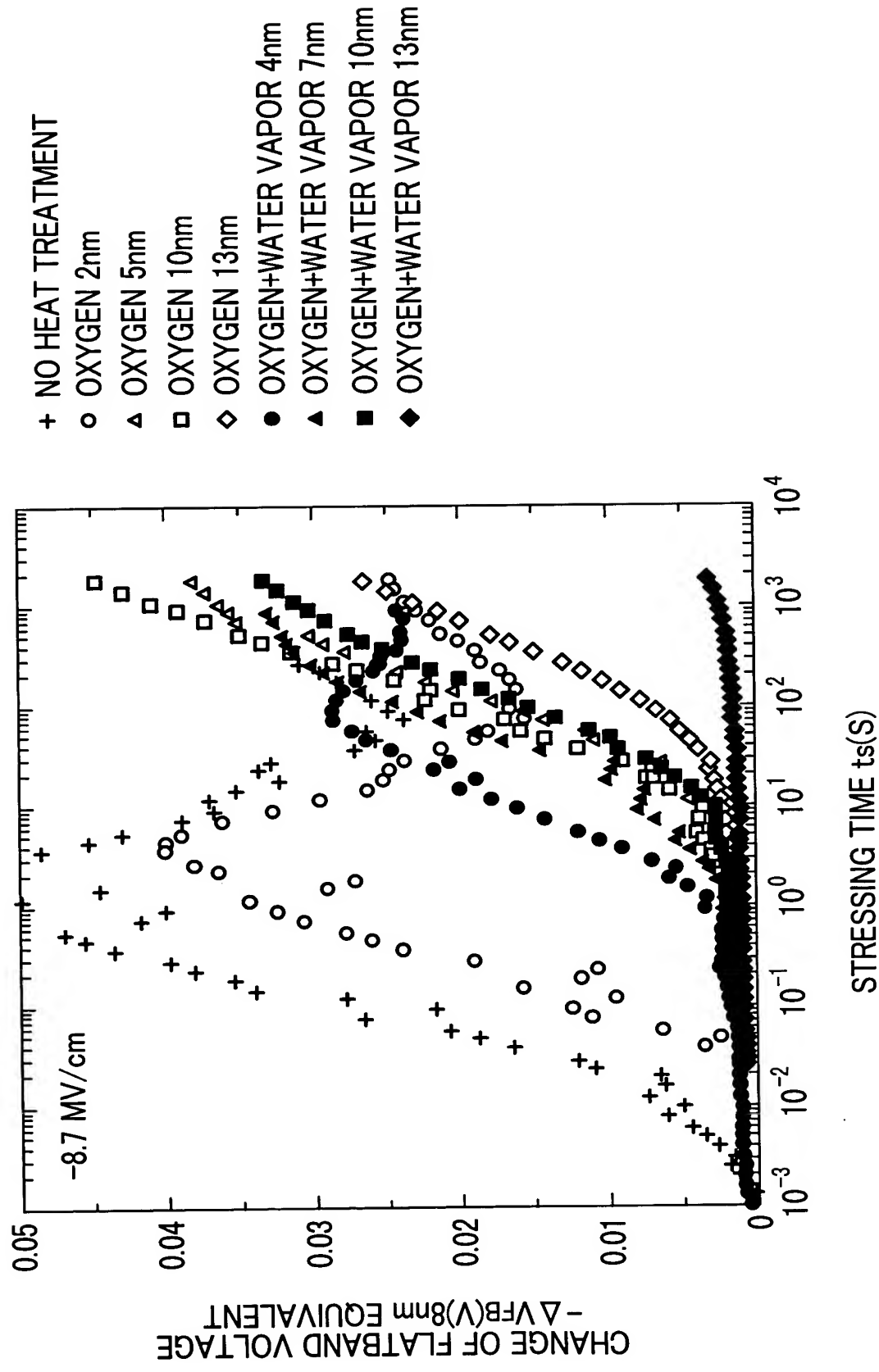


FIG.6



# FIG.7



# FIG.8

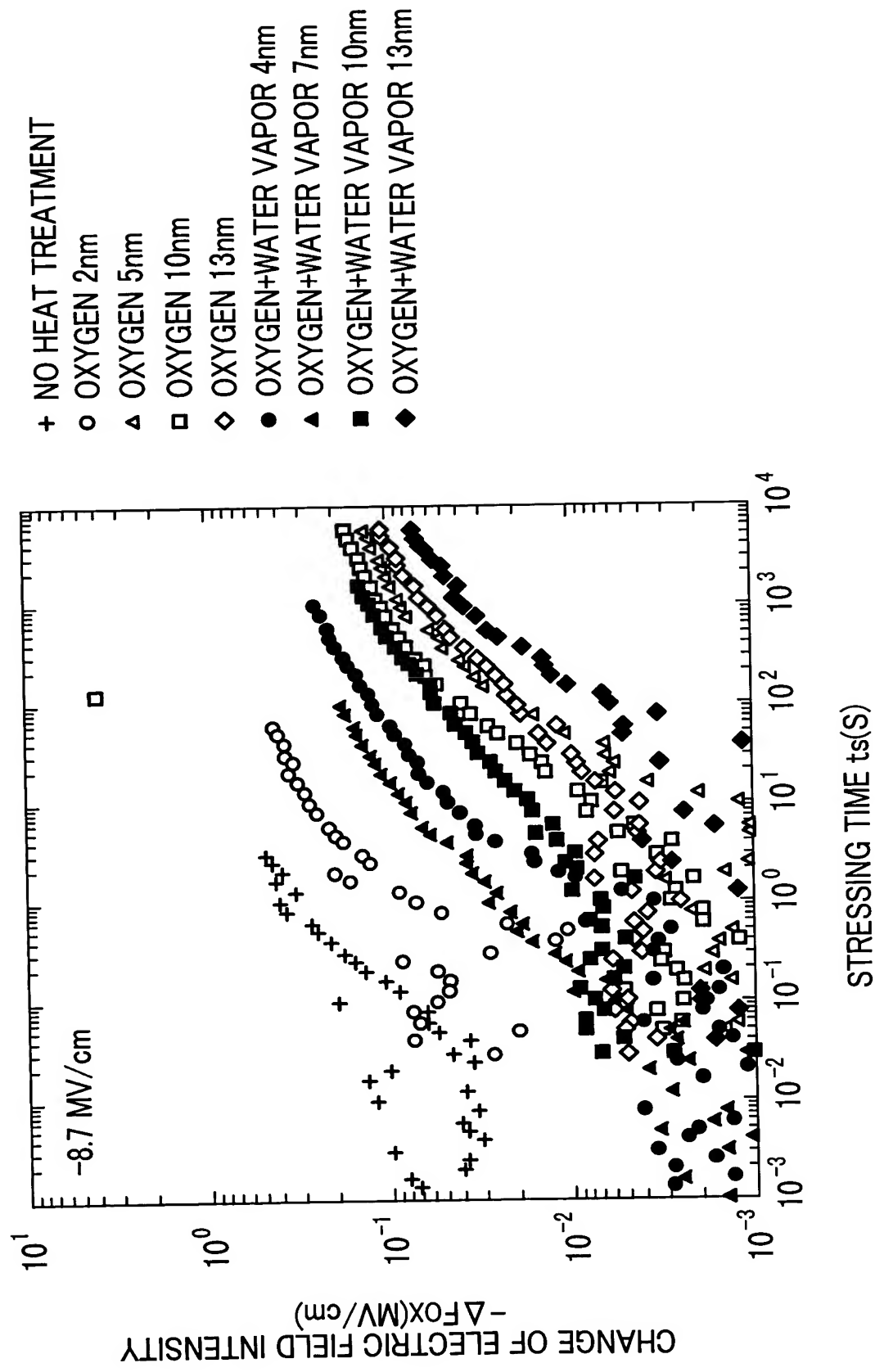
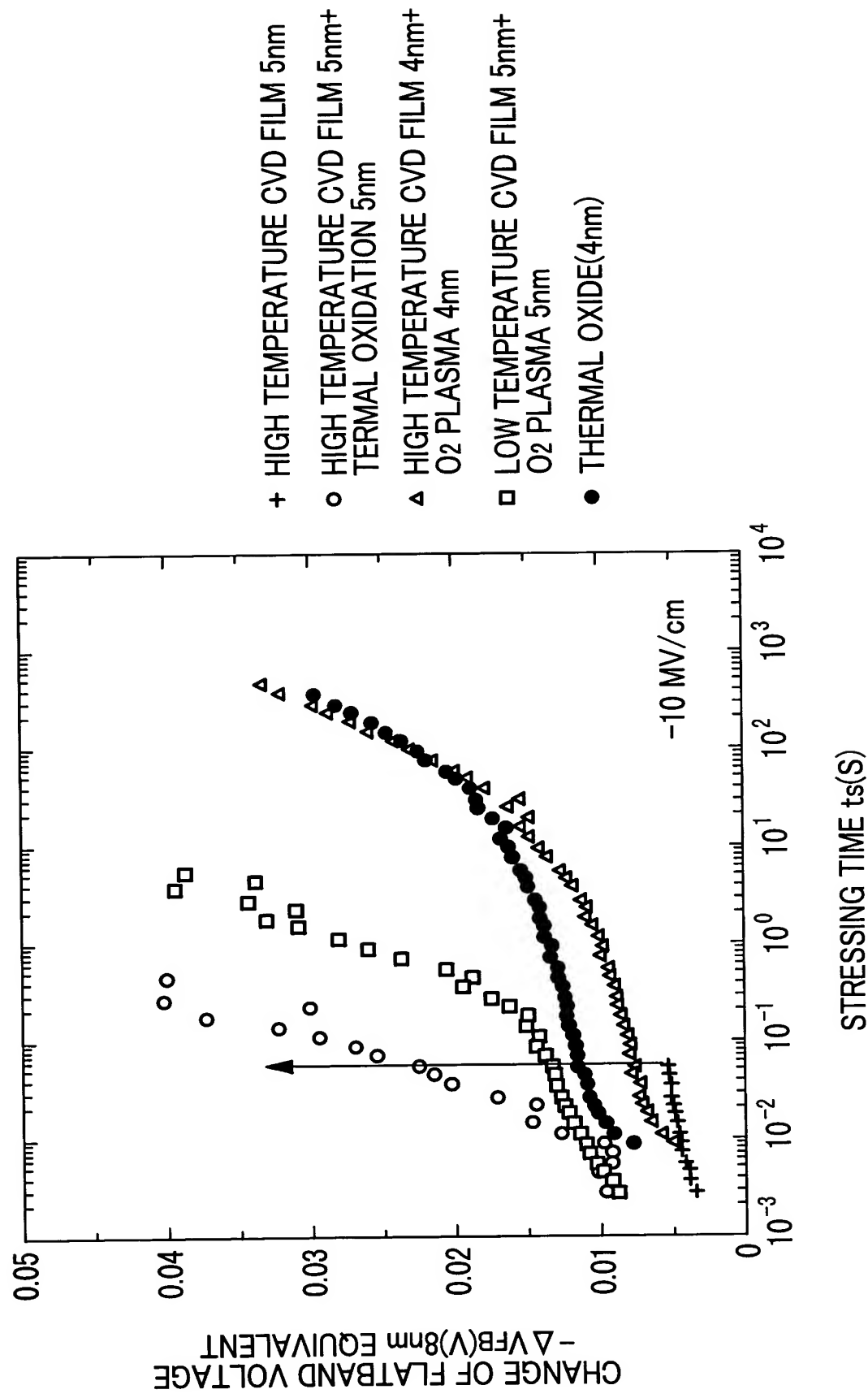




FIG.9



# FIG.10

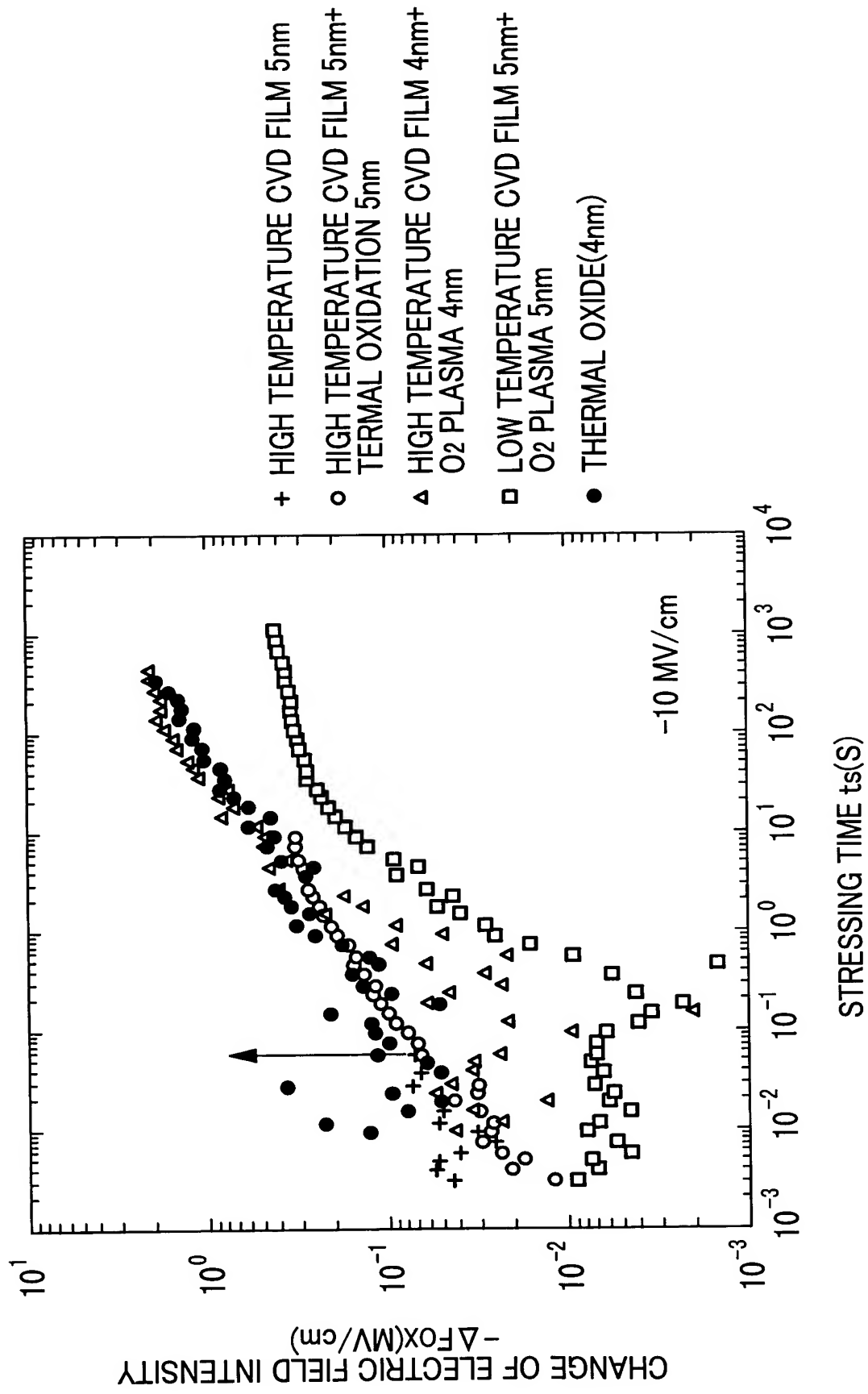


FIG.11

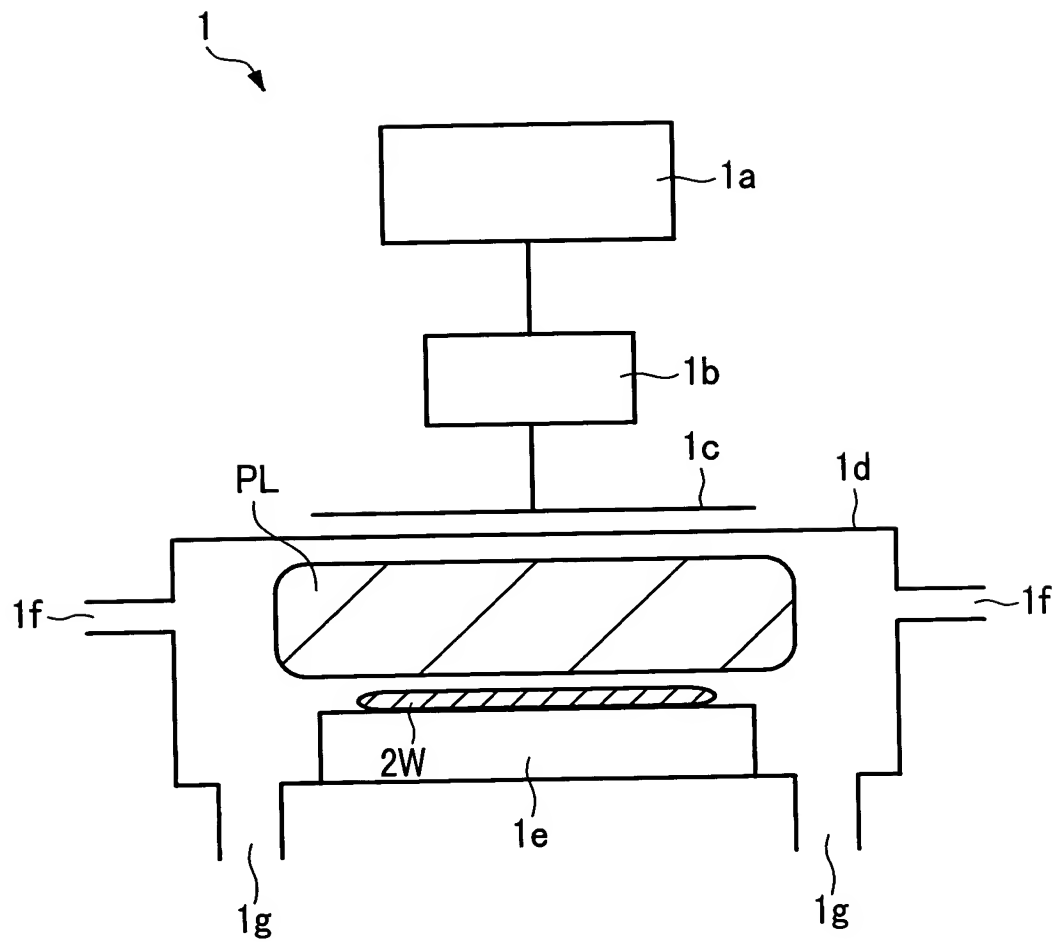
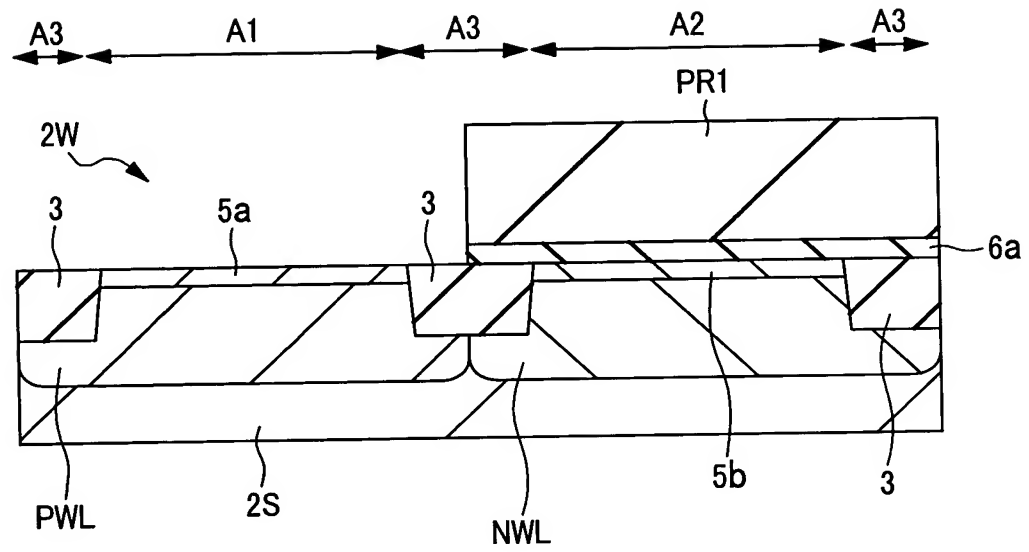
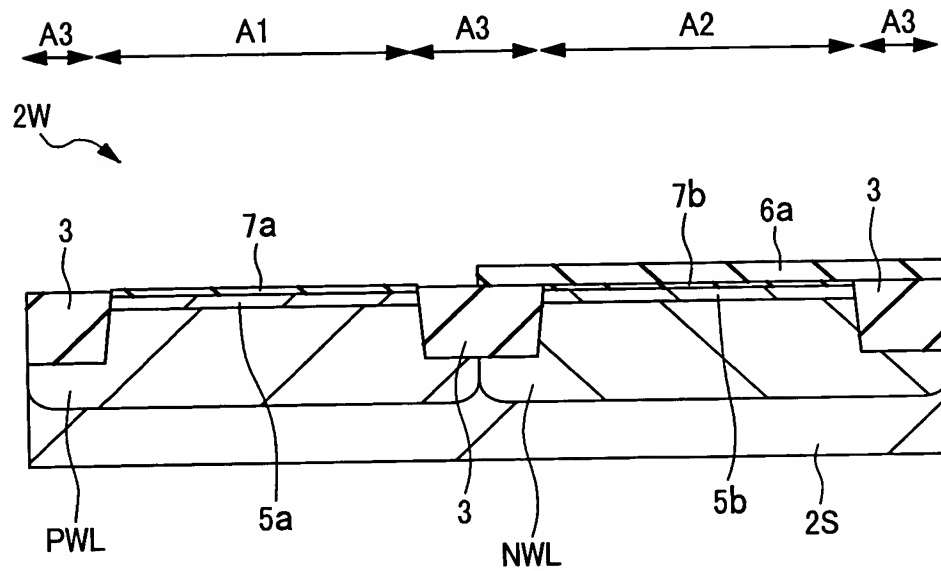


Figure 1 is a schematic diagram of a semiconductor device. The top part shows a plan view with segments A1, A2, and A3, and arrows indicating a width of 2W. The bottom part is a cross-sectional view showing layers 3, 6a, 5a, 5b, and 2S, with a patterned layer PWL and a notched layer NWL.

# FIG.14

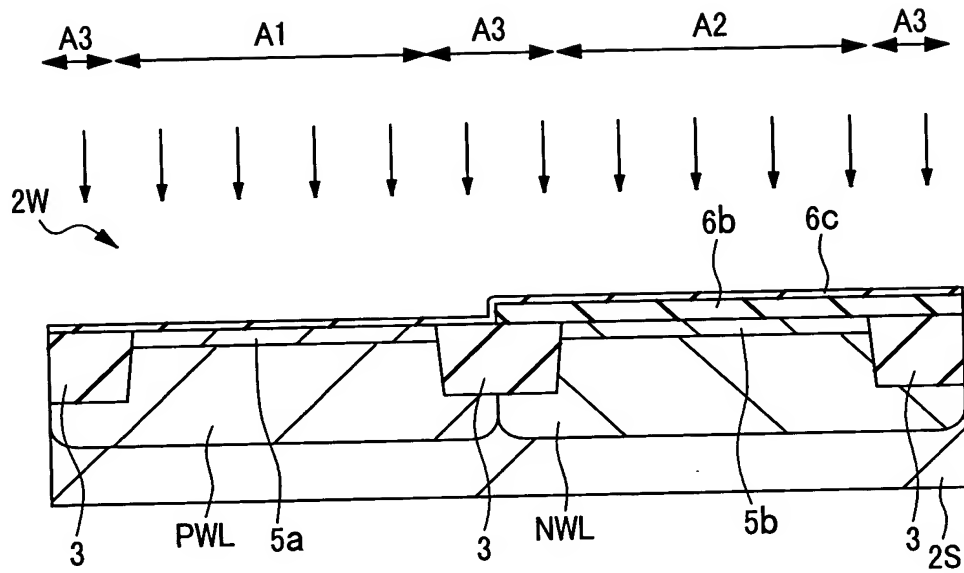


# FIG.15

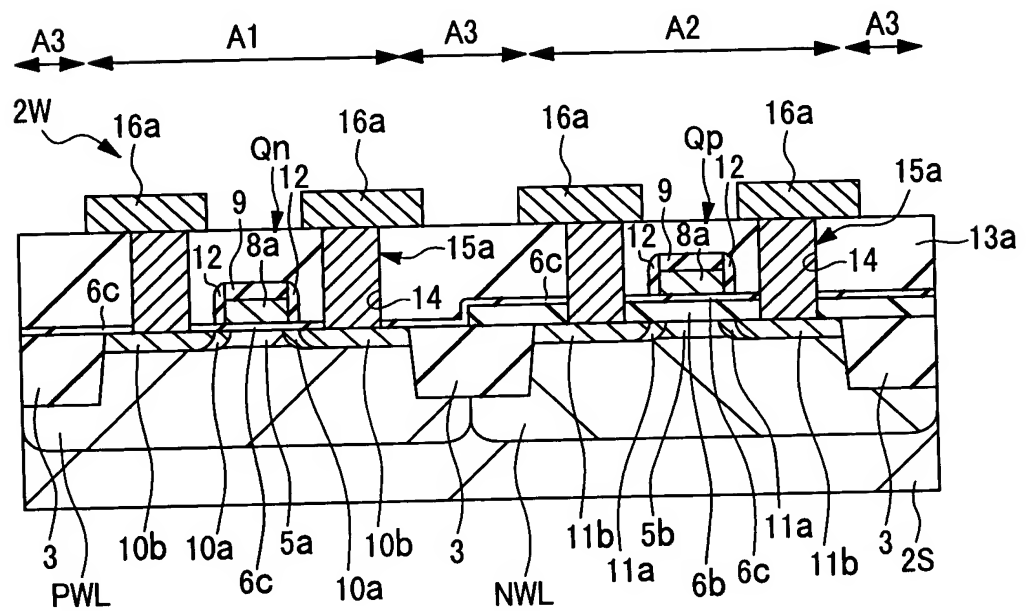


[illegible]

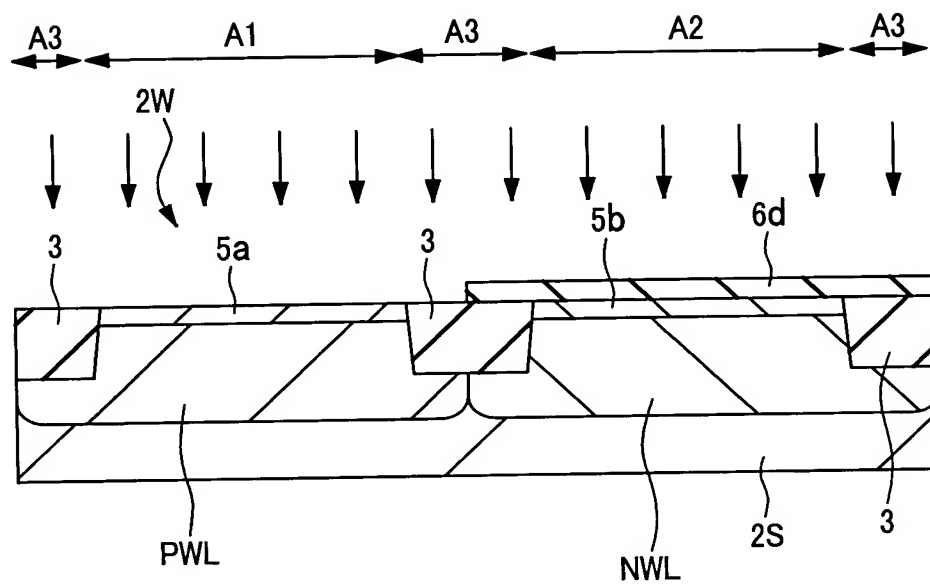
# FIG.18



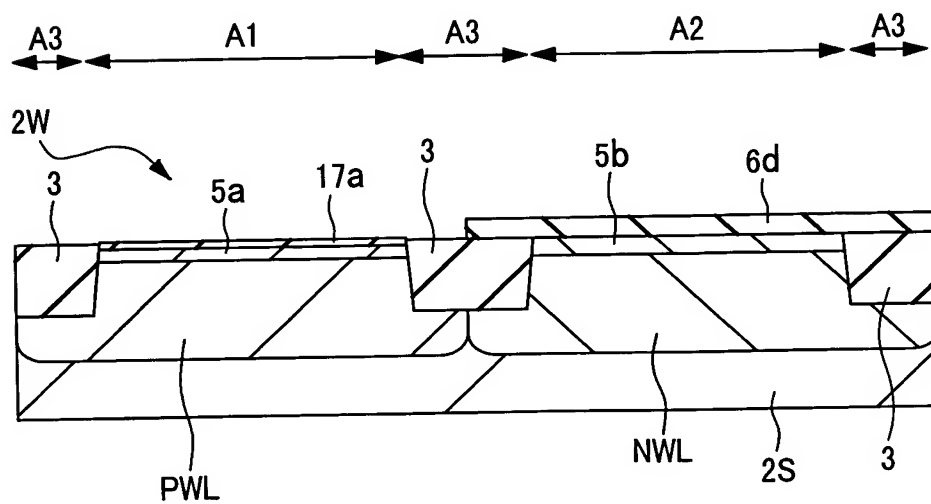
# FIG.19



# FIG.20

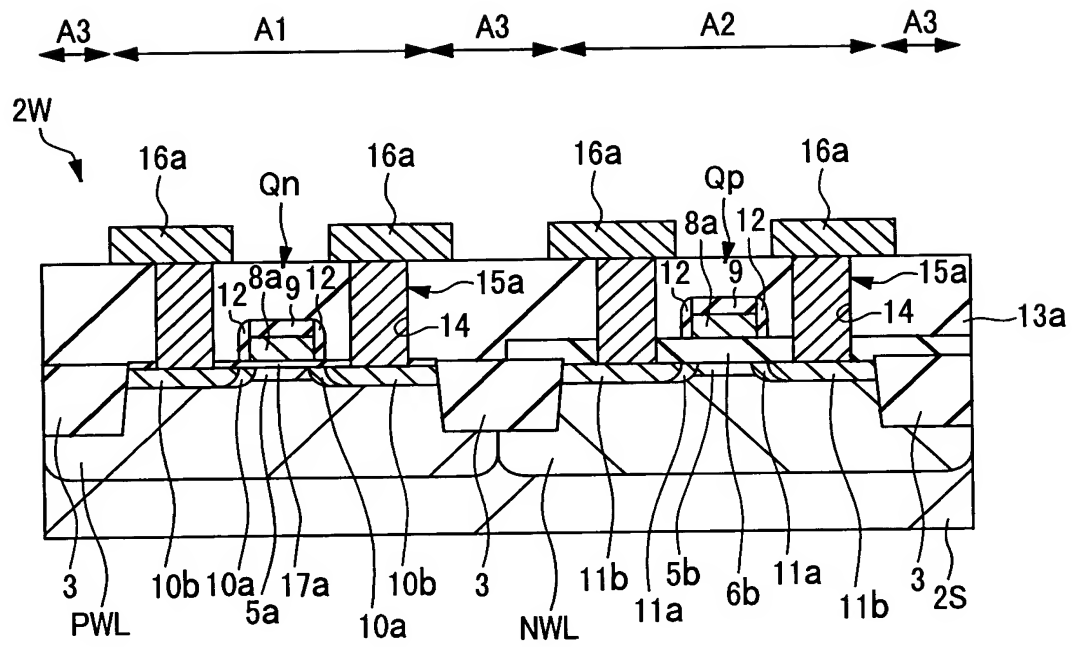


# FIG.21

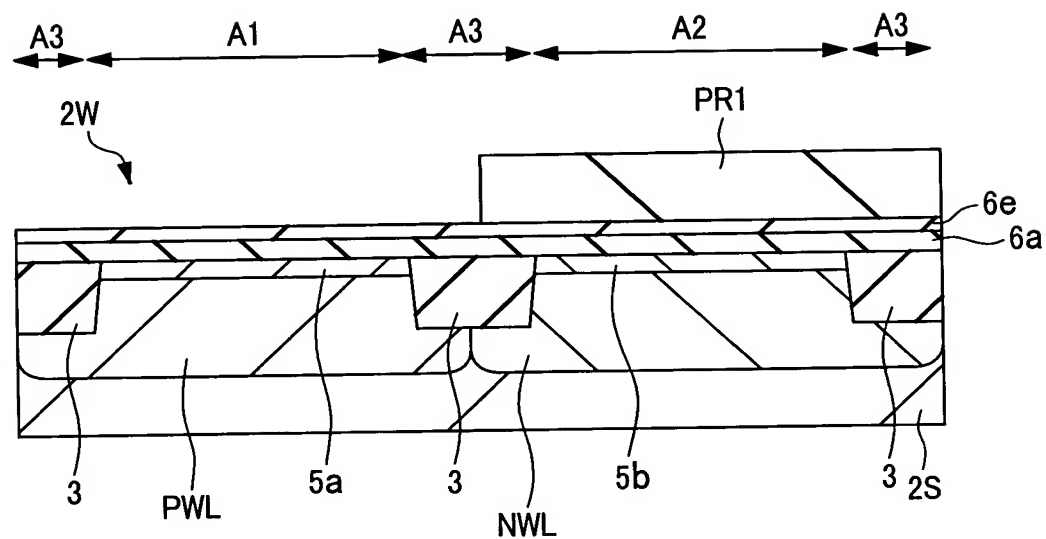




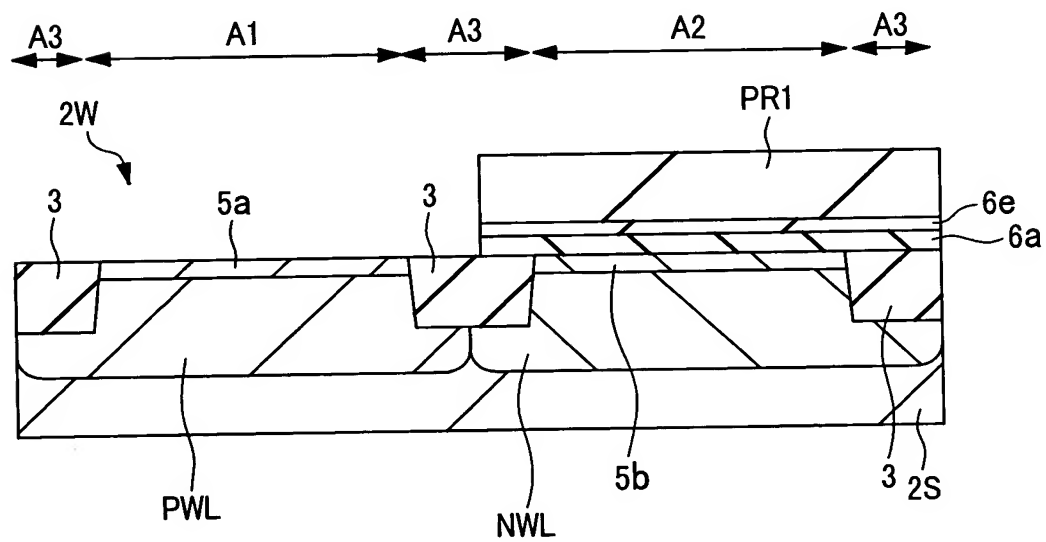
# FIG.22



# FIG.23

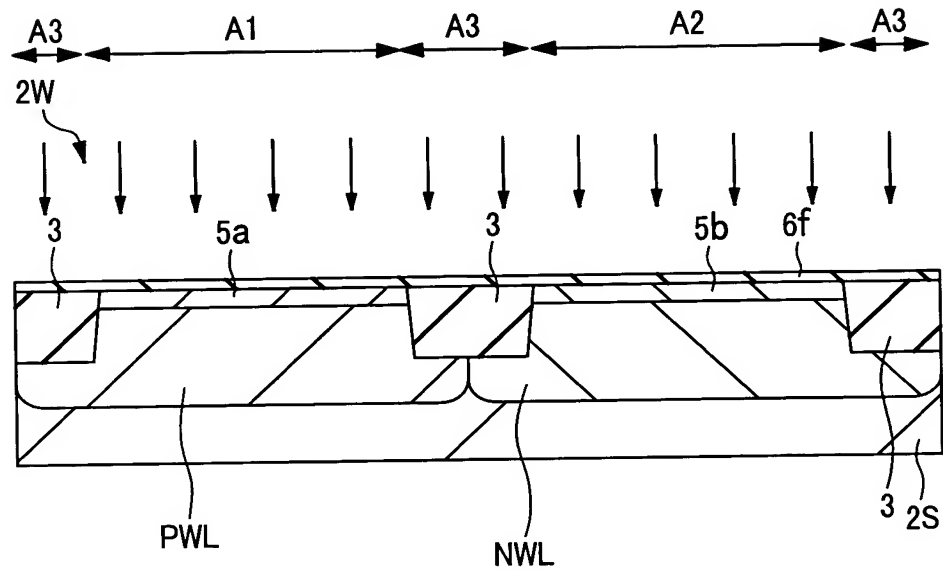


# FIG.24

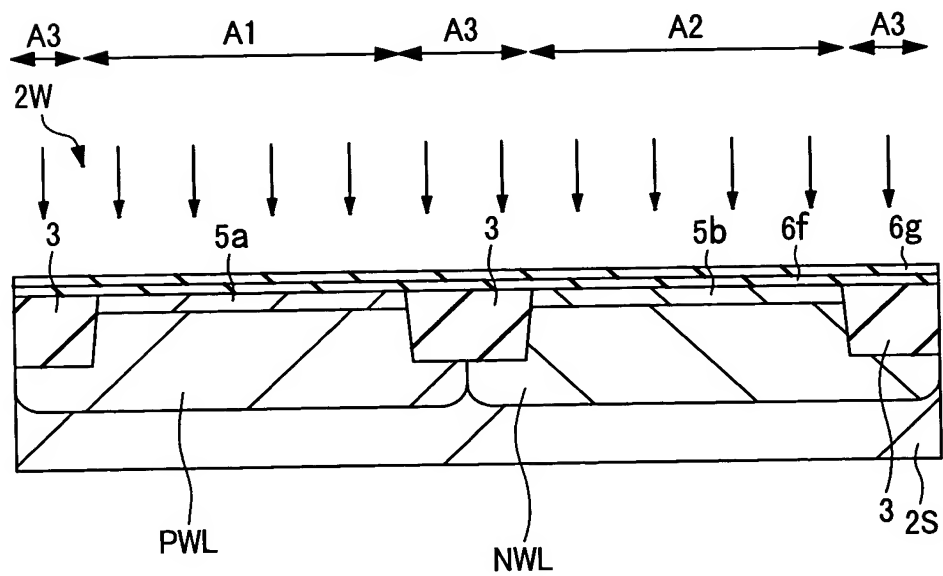


[illegible][illegible]

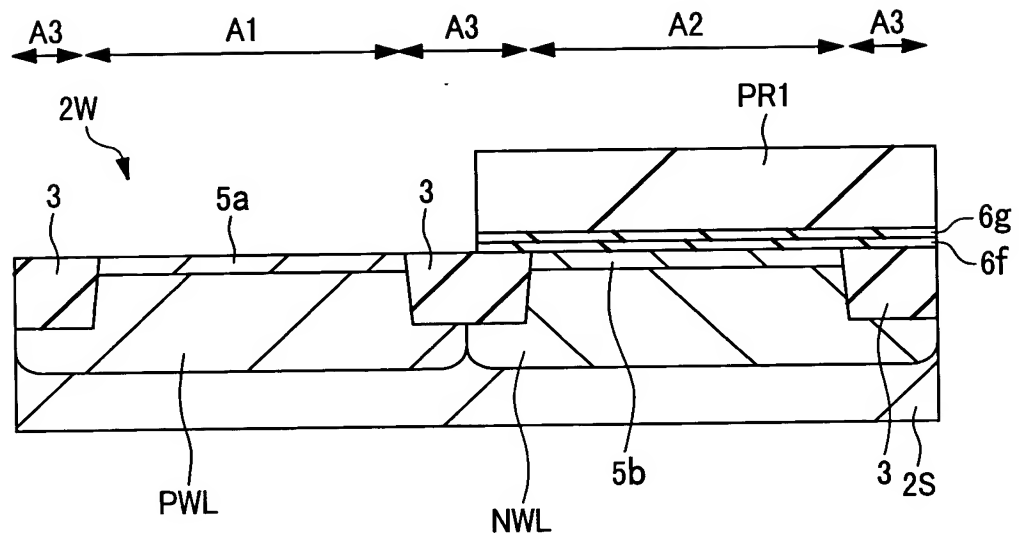
# FIG.27



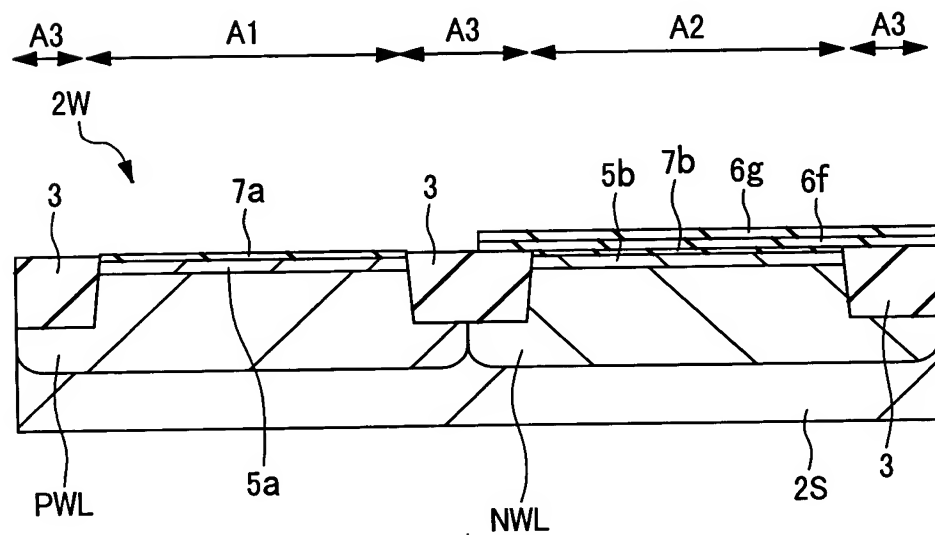
# FIG.28



# FIG.29

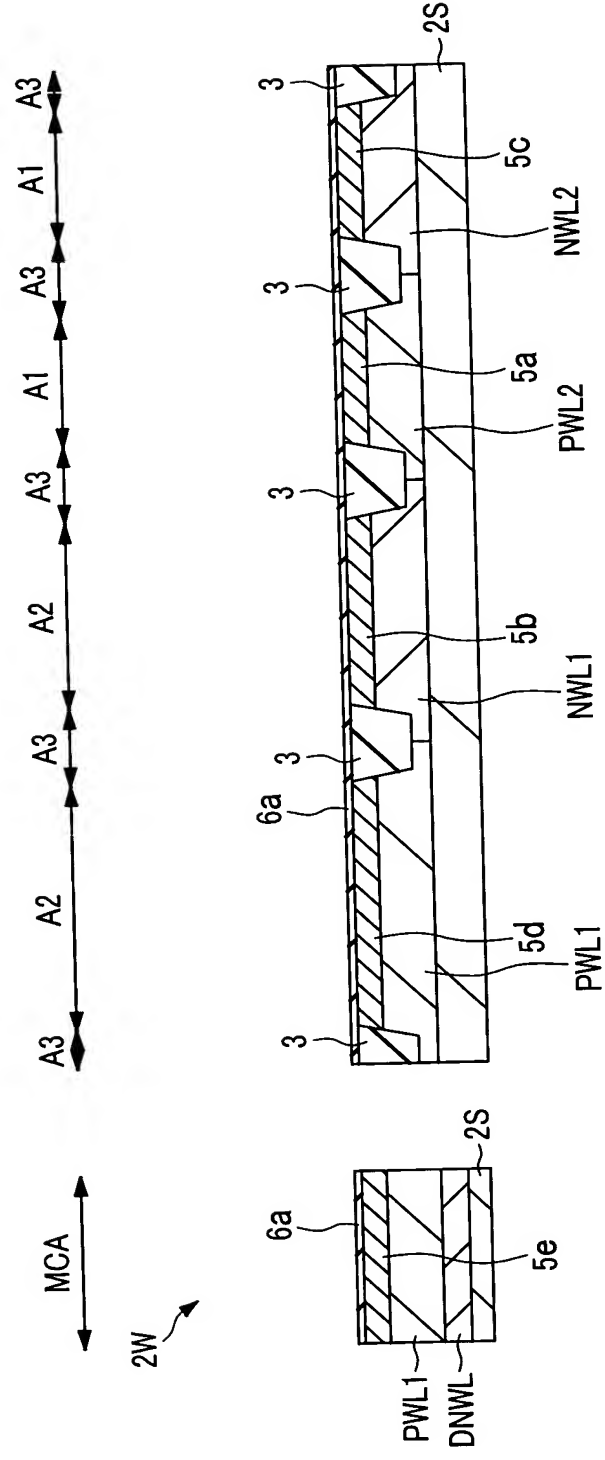


# FIG.30

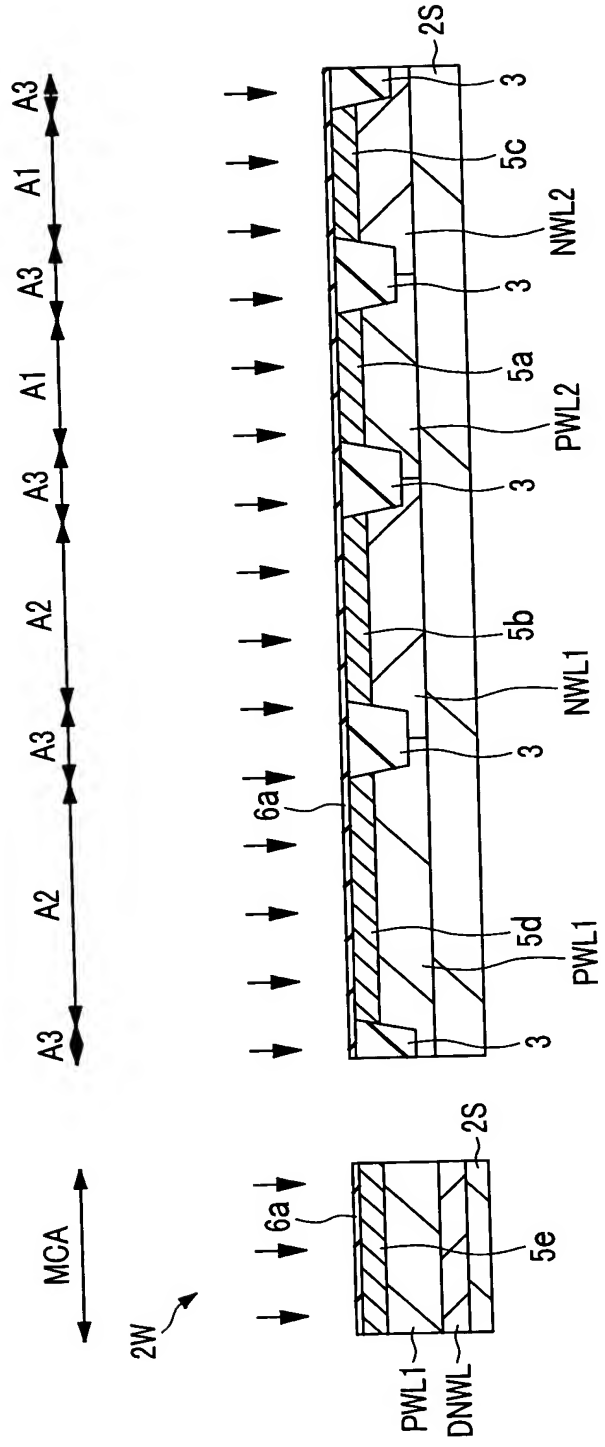


[illegible]

# FIG.32



# FIG.33

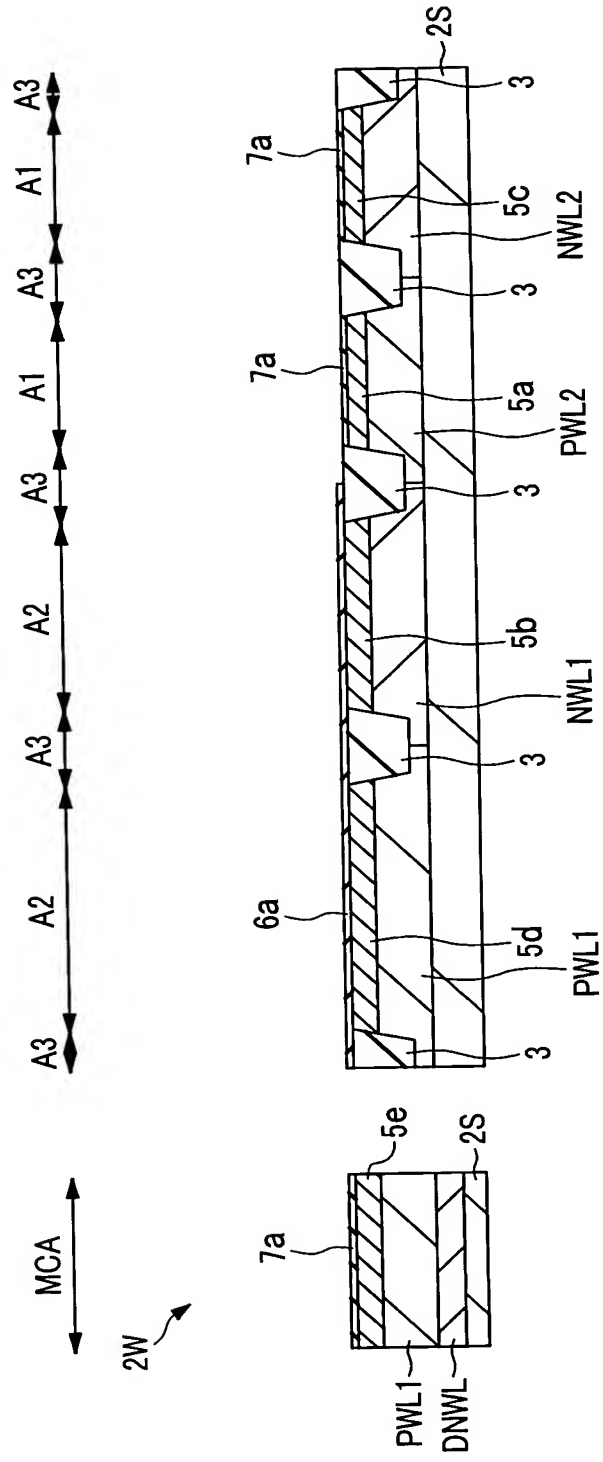




• •

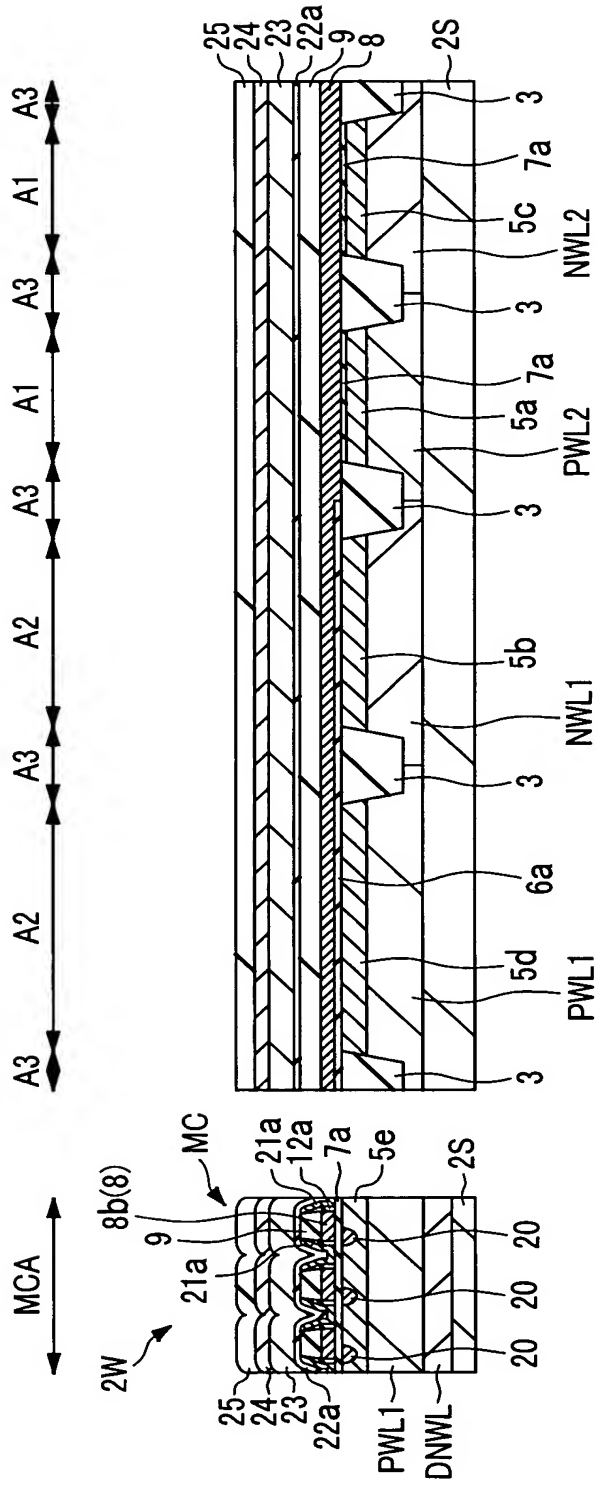


FIG. 35

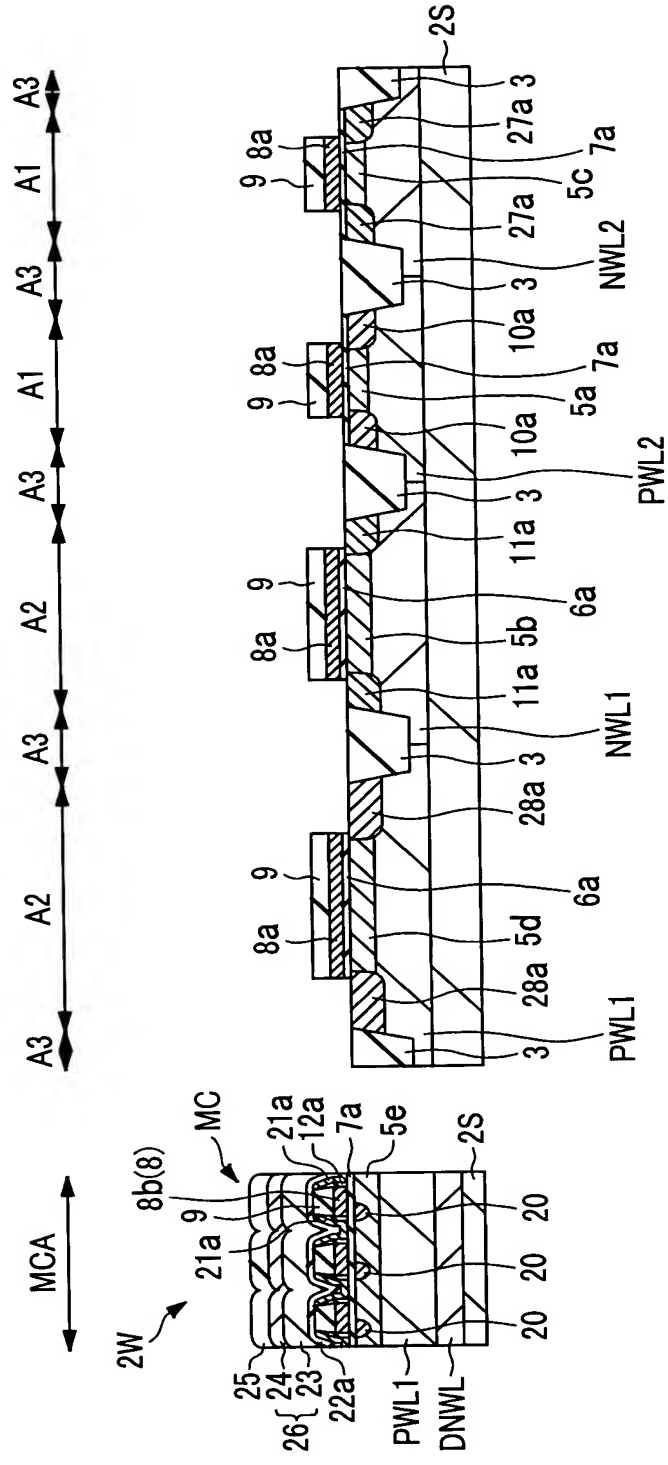




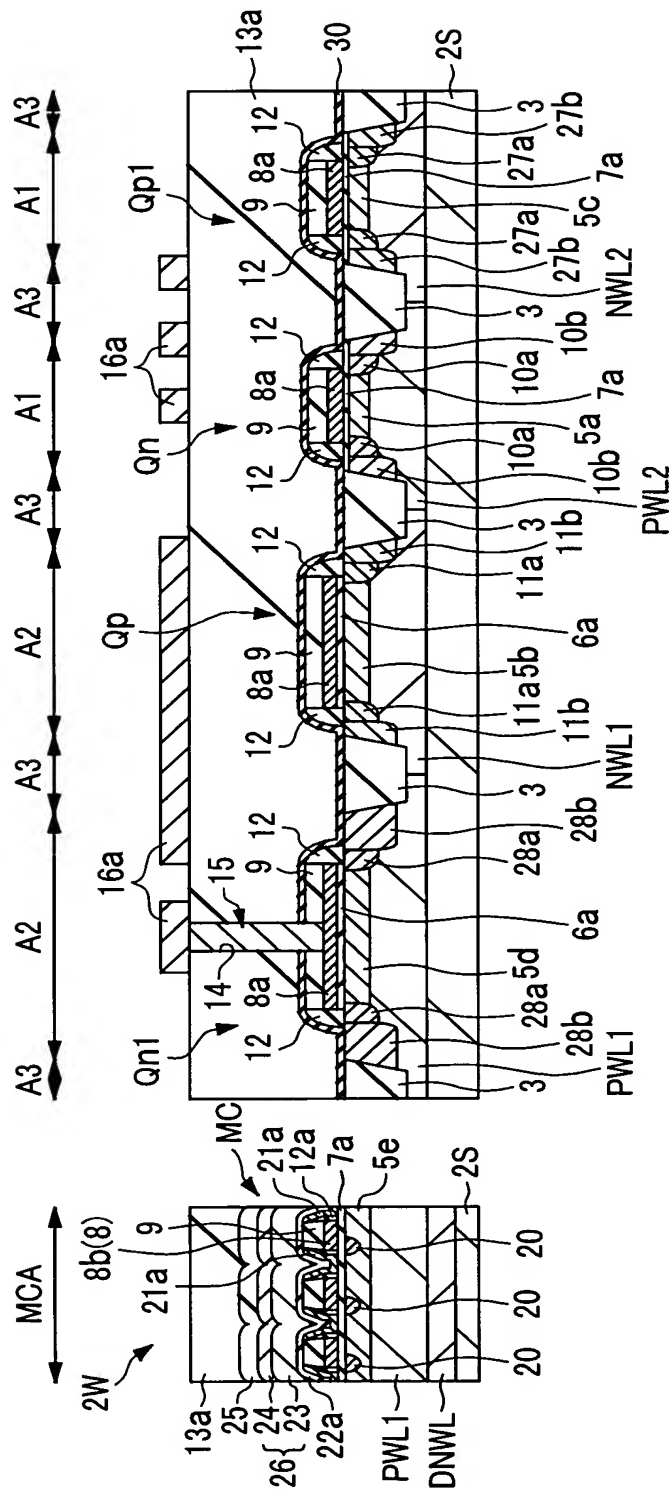
# FIG.37



# FIG.38



**FIG. 39**



# FIG.40



2W

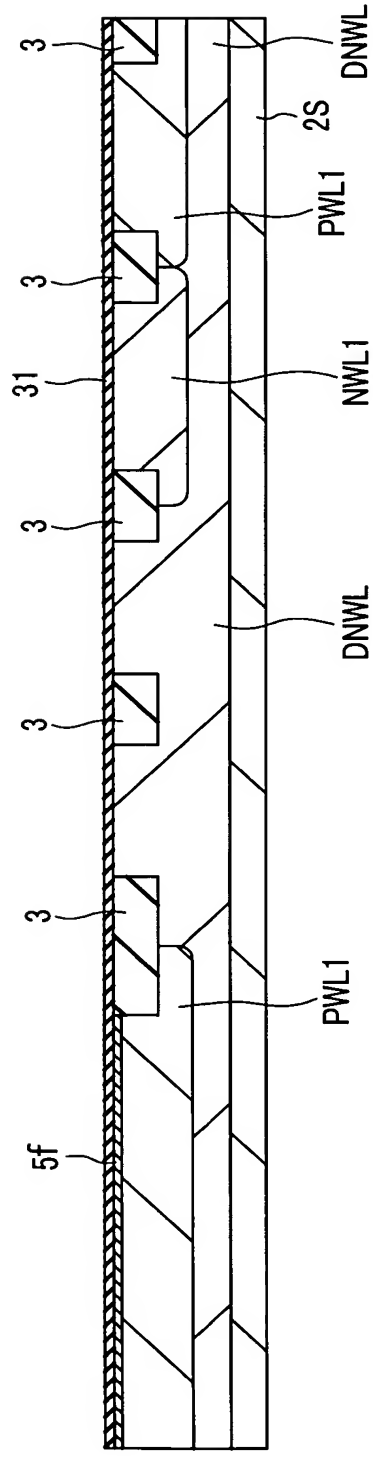
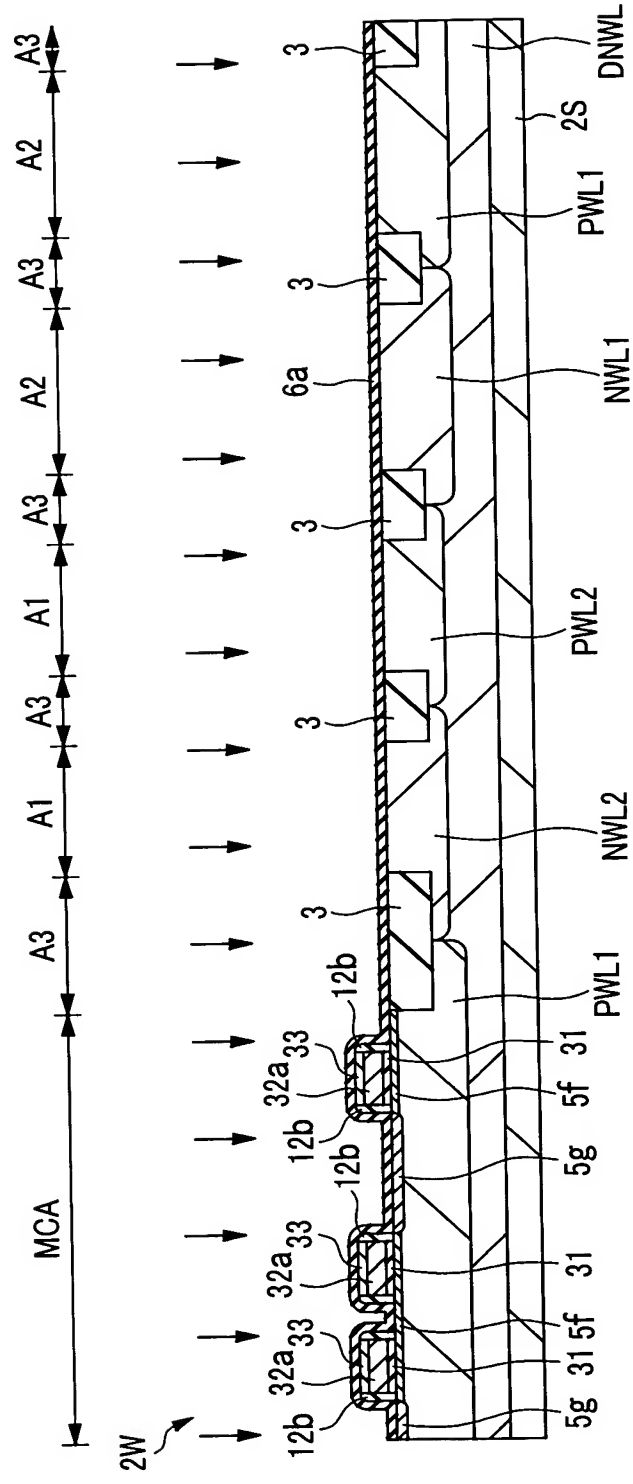
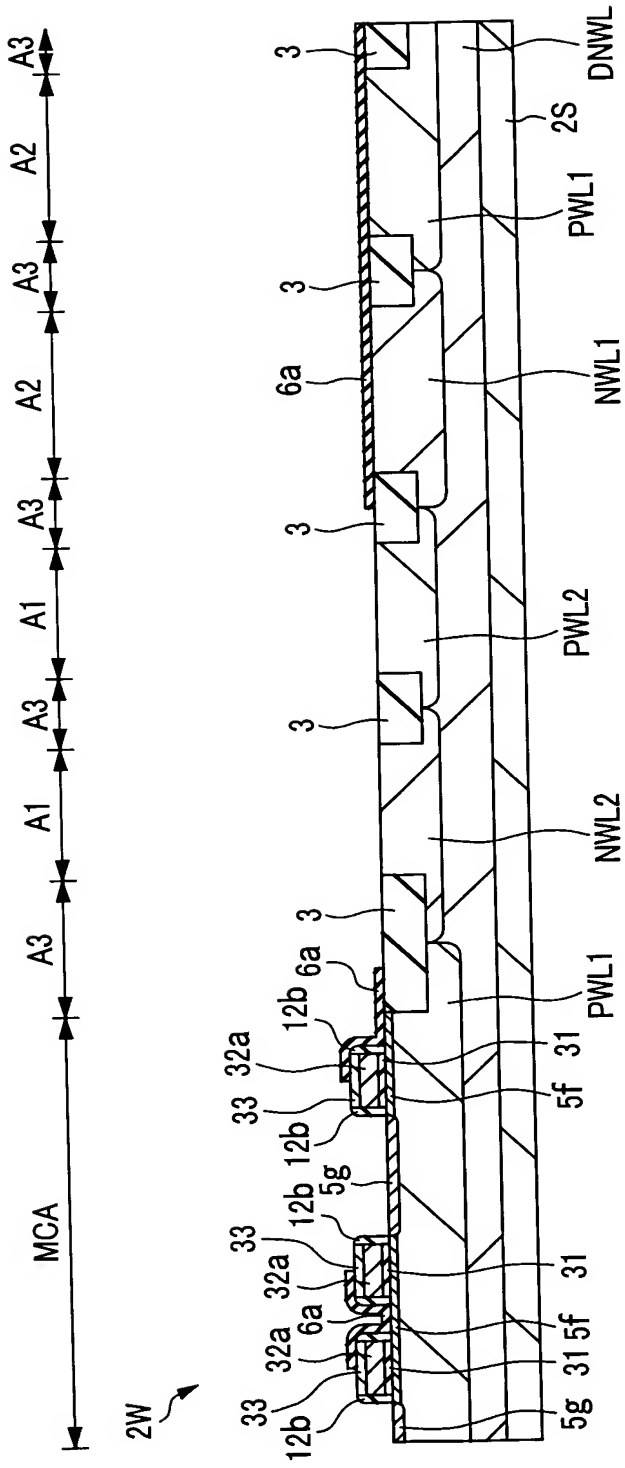


FIG.41





**FIG. 42**



# FIG.43

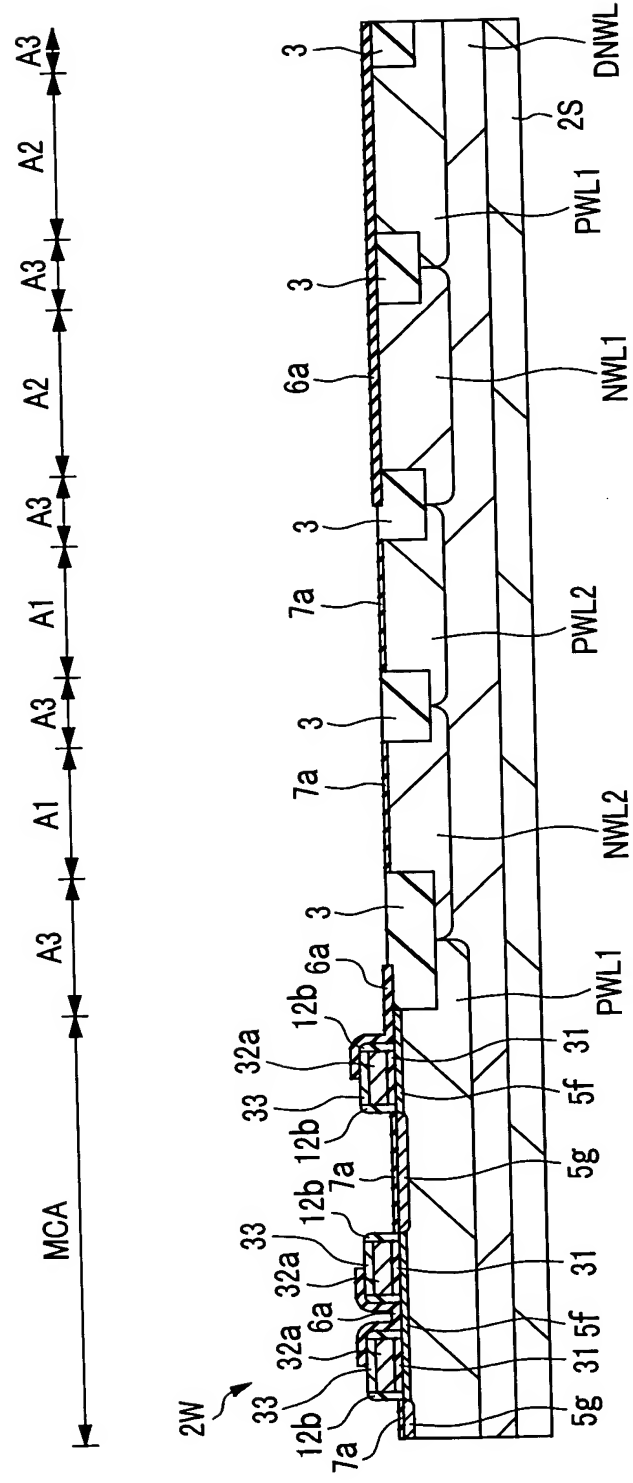


FIG.44

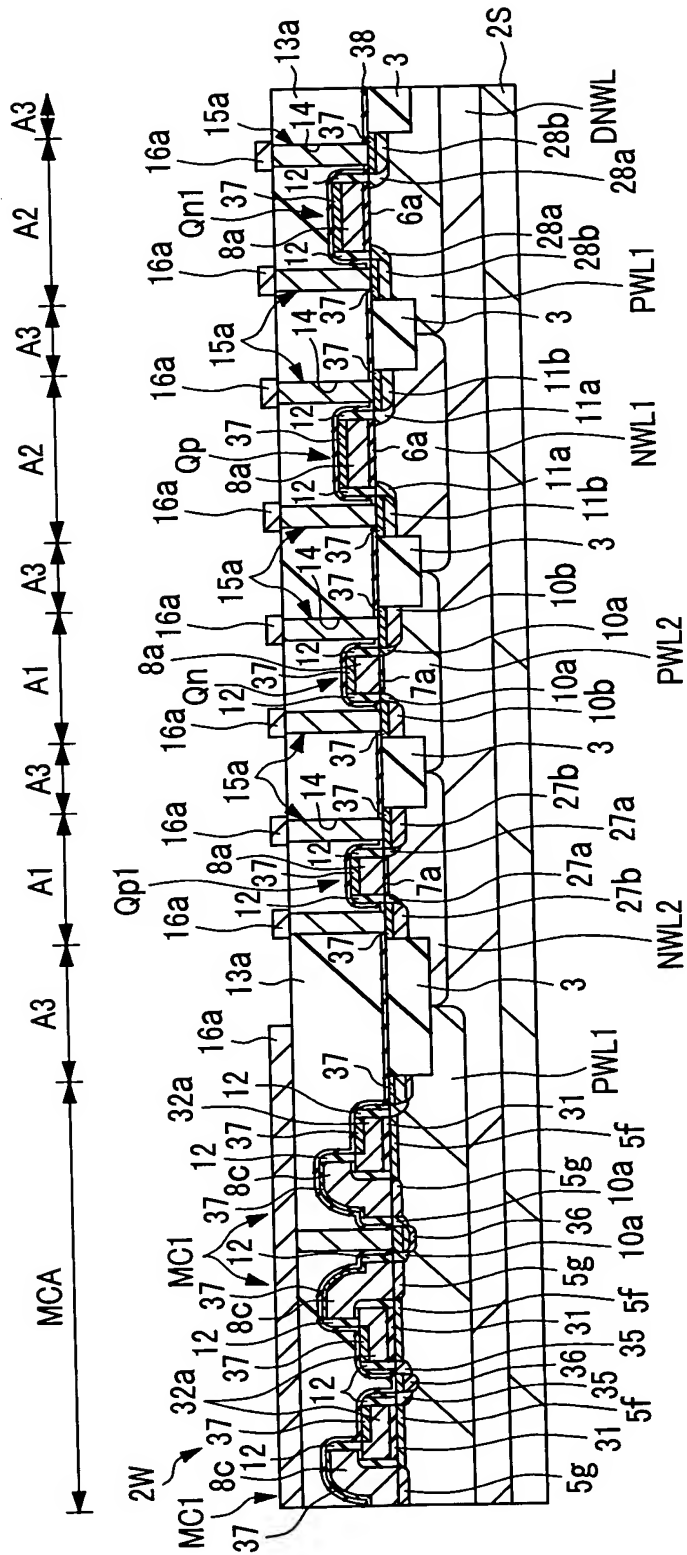
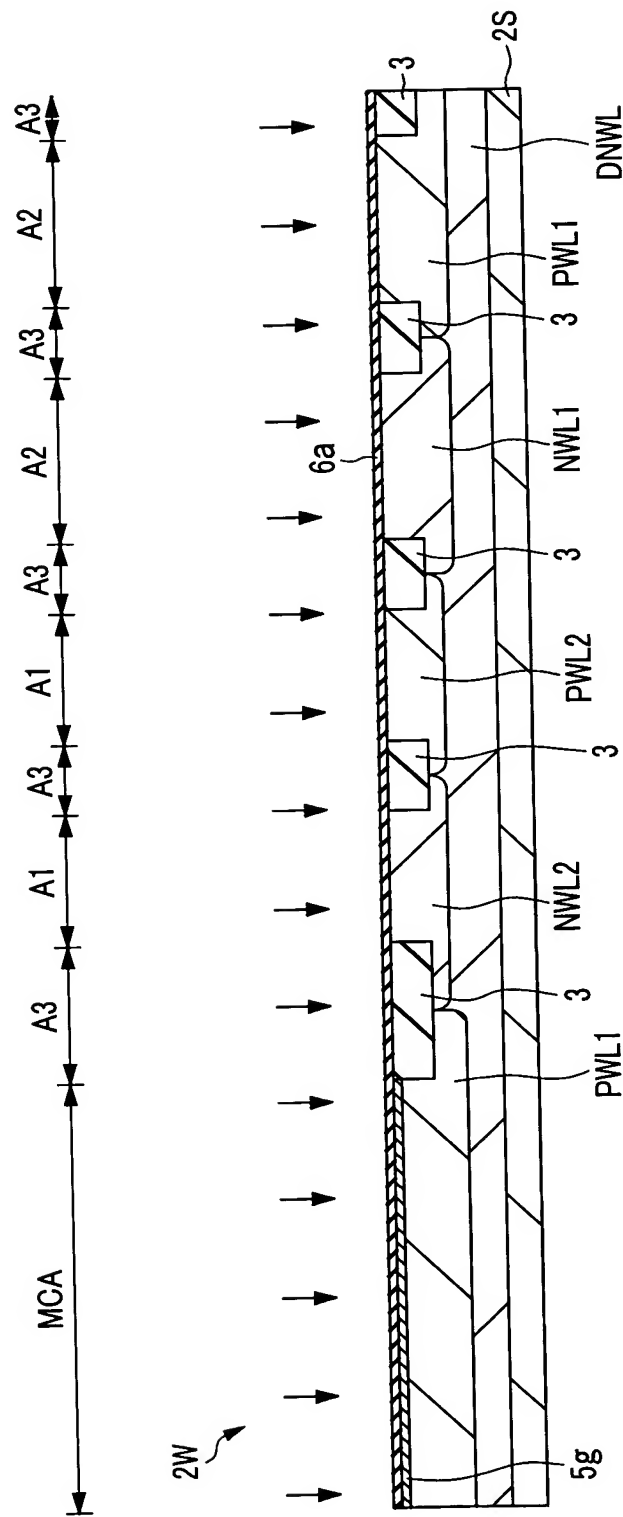
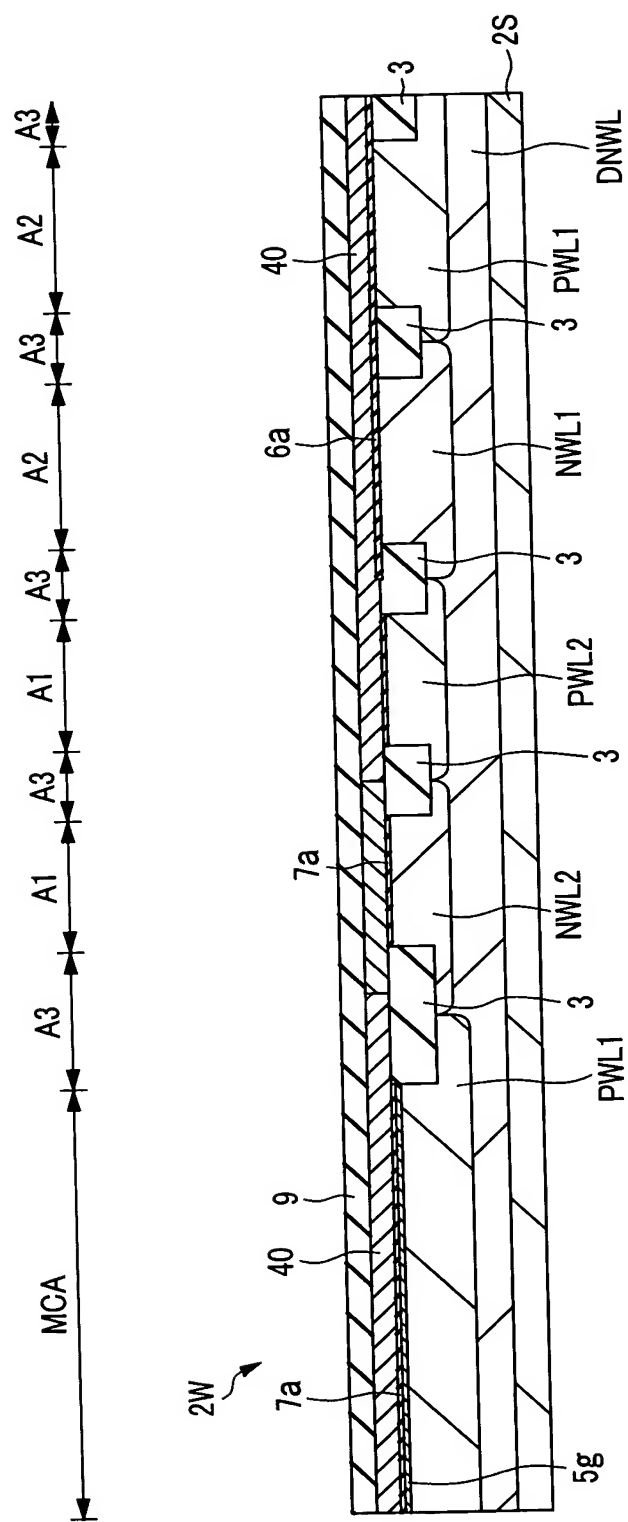


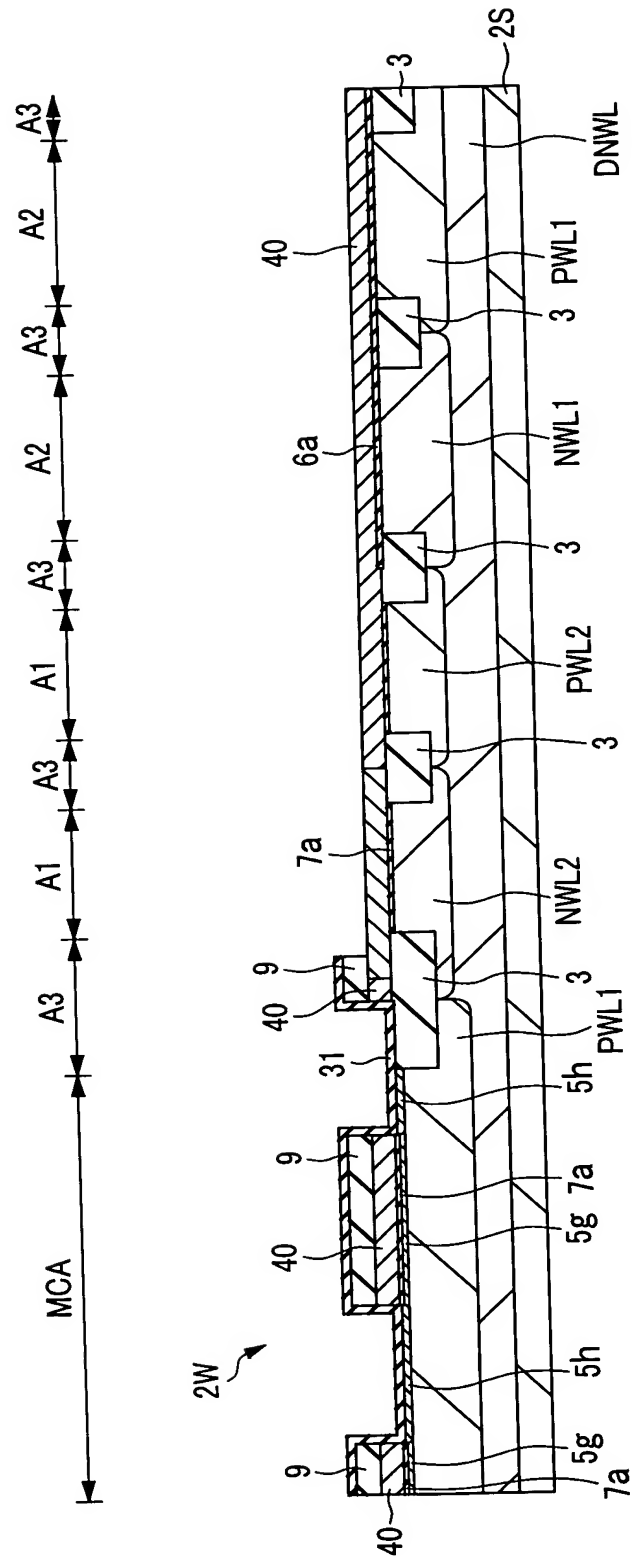
FIG.45



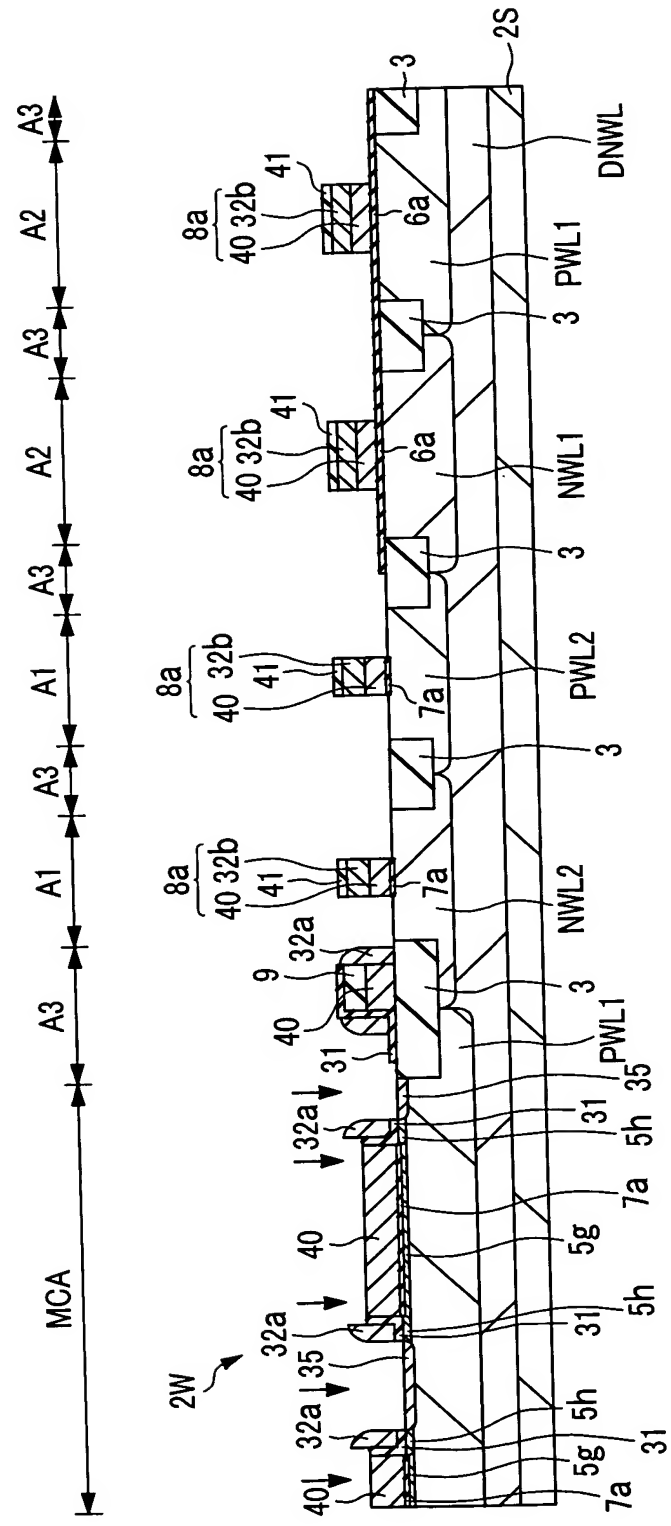
**FIG. 46**



**FIG. 47**



**FIG. 48**



**FIG. 49**

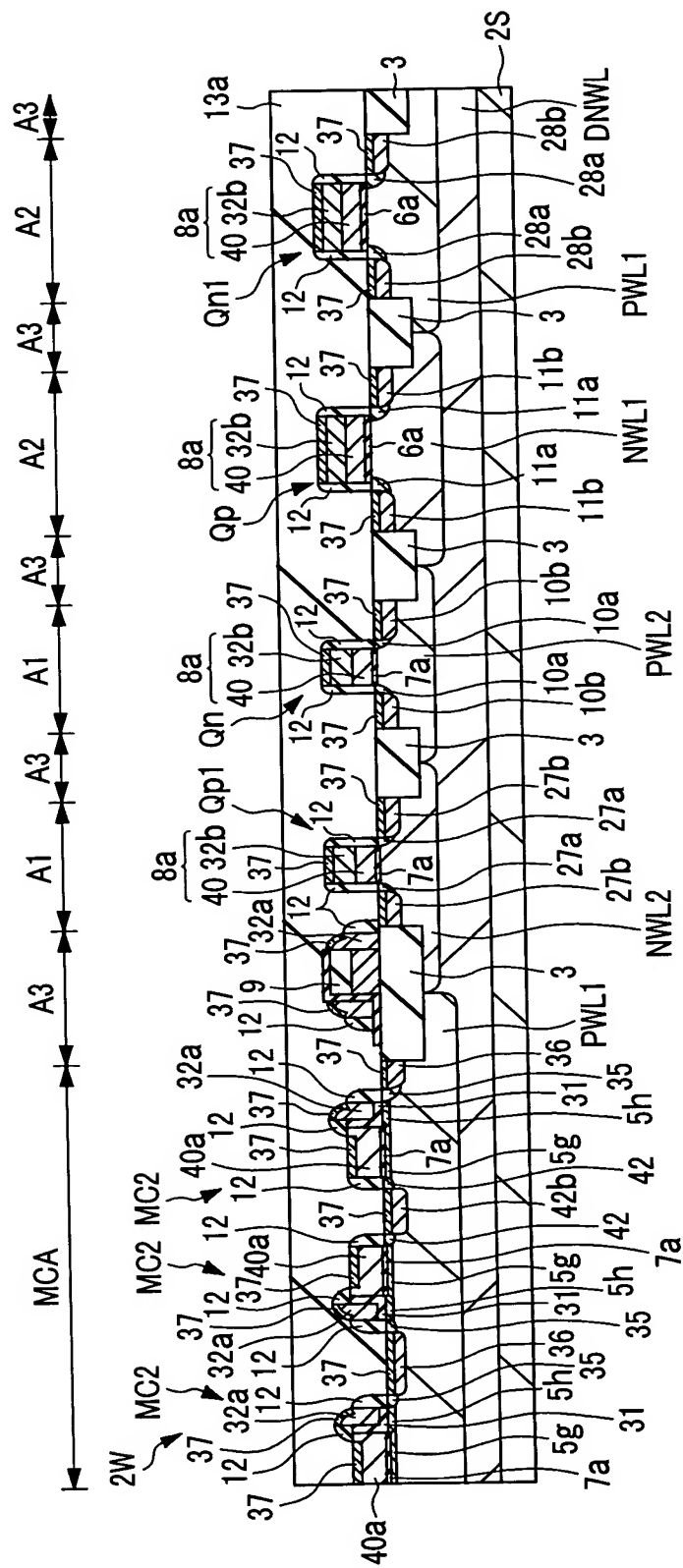




FIG.50

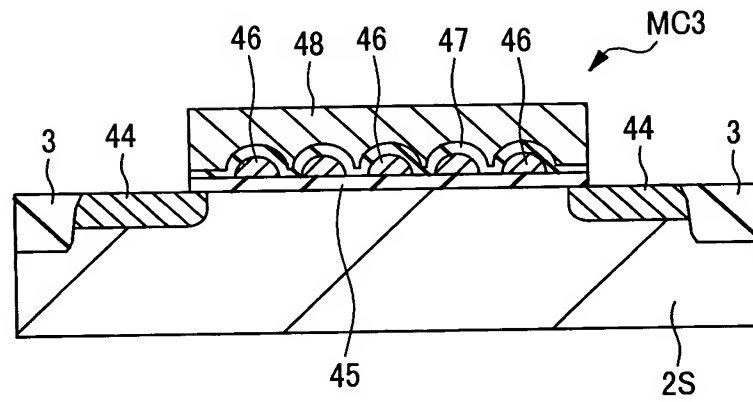
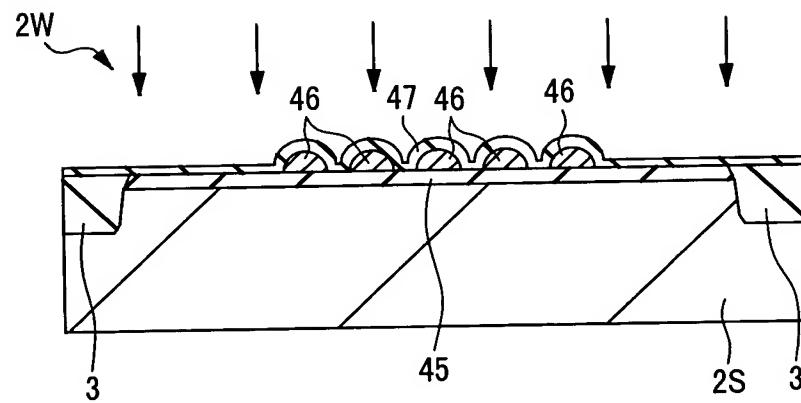
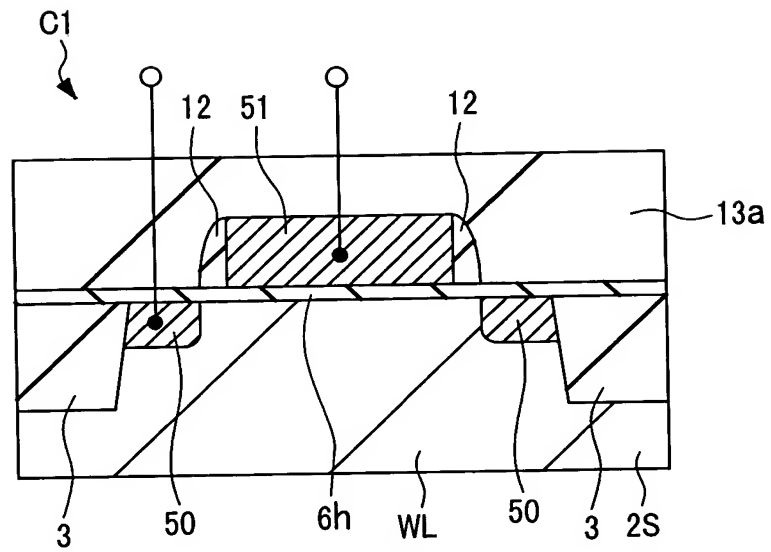


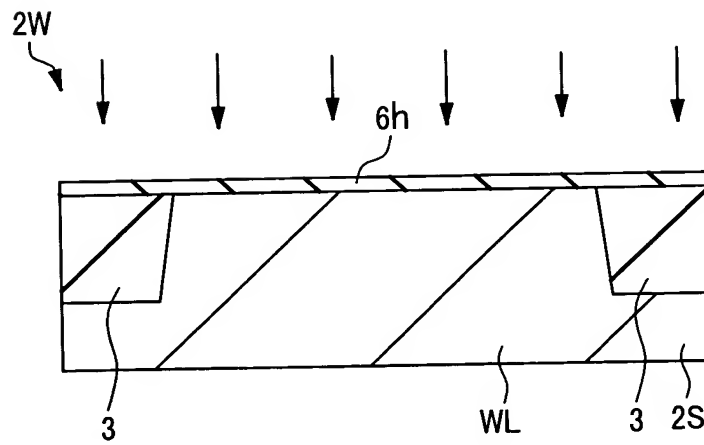
FIG.51



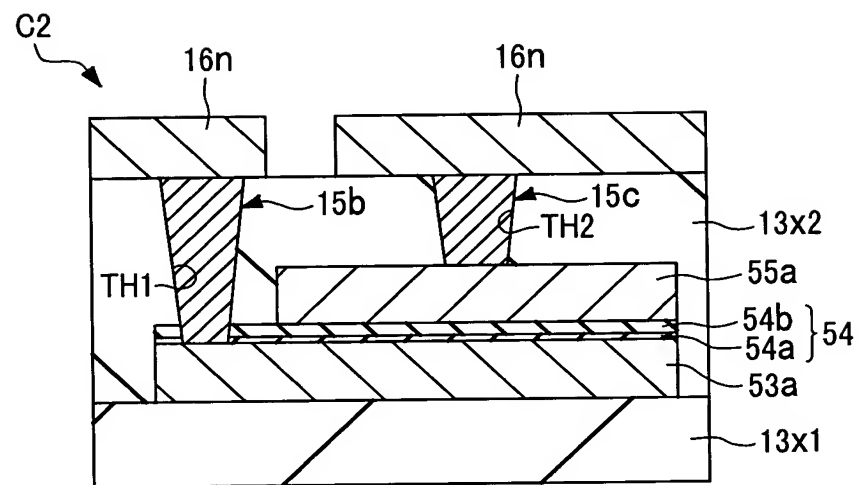
# FIG.52



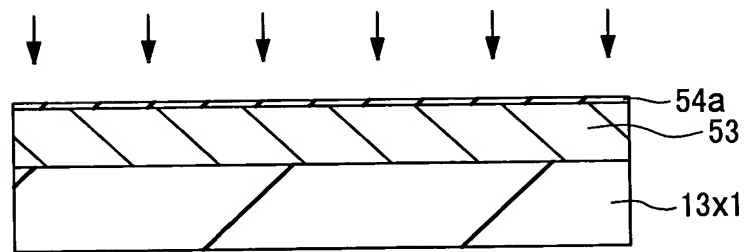
# FIG.53



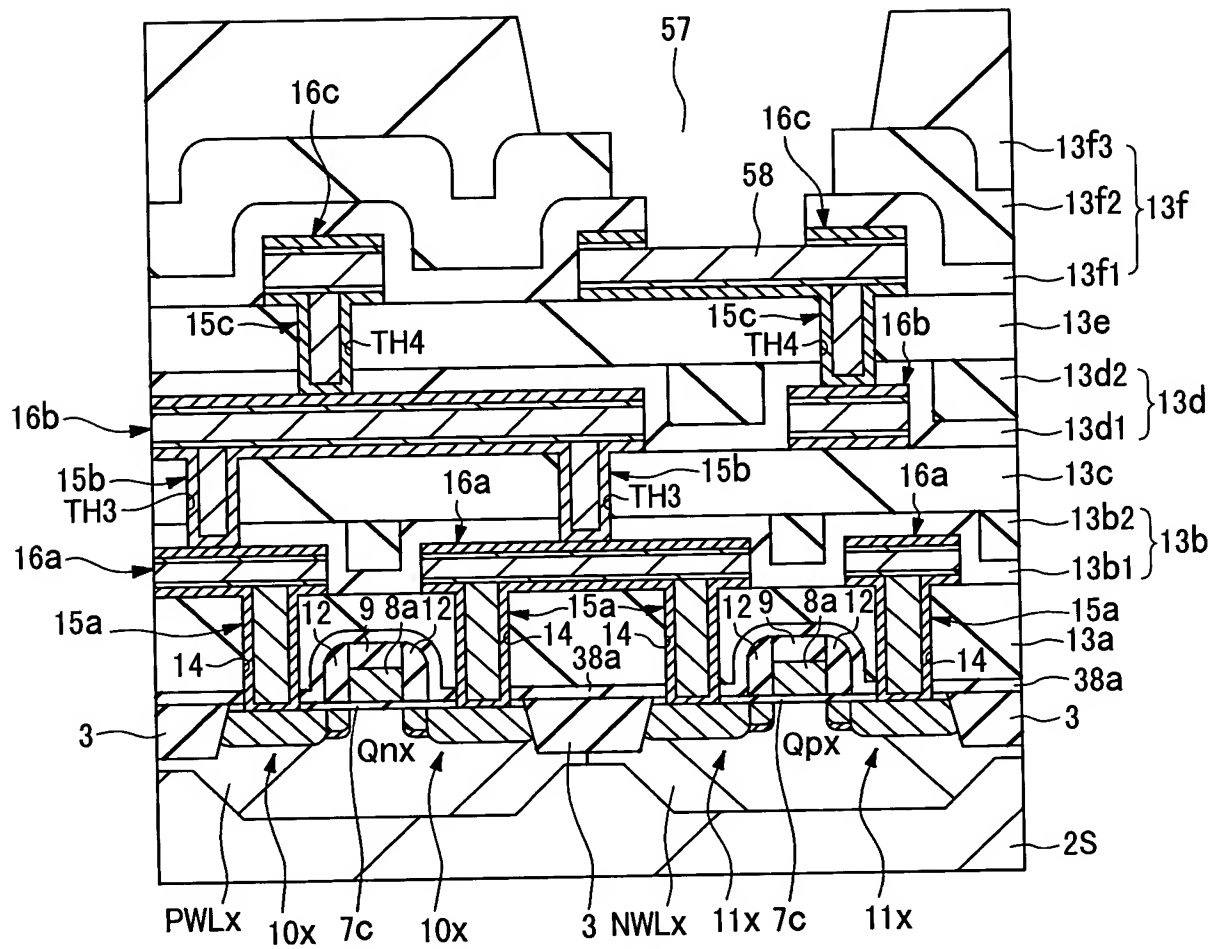
# FIG.54



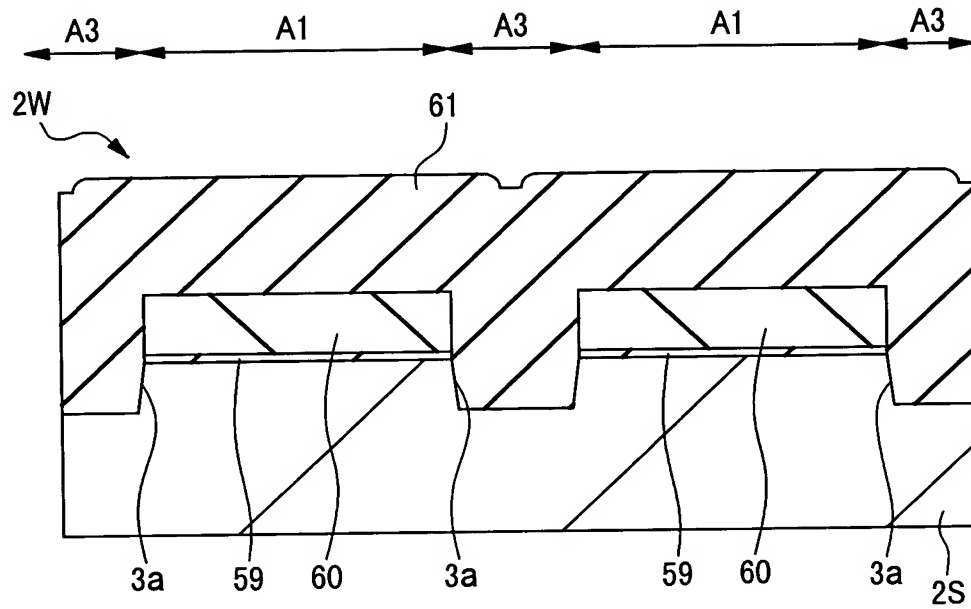
# FIG.55



# FIG.56



# FIG.57



# FIG.58

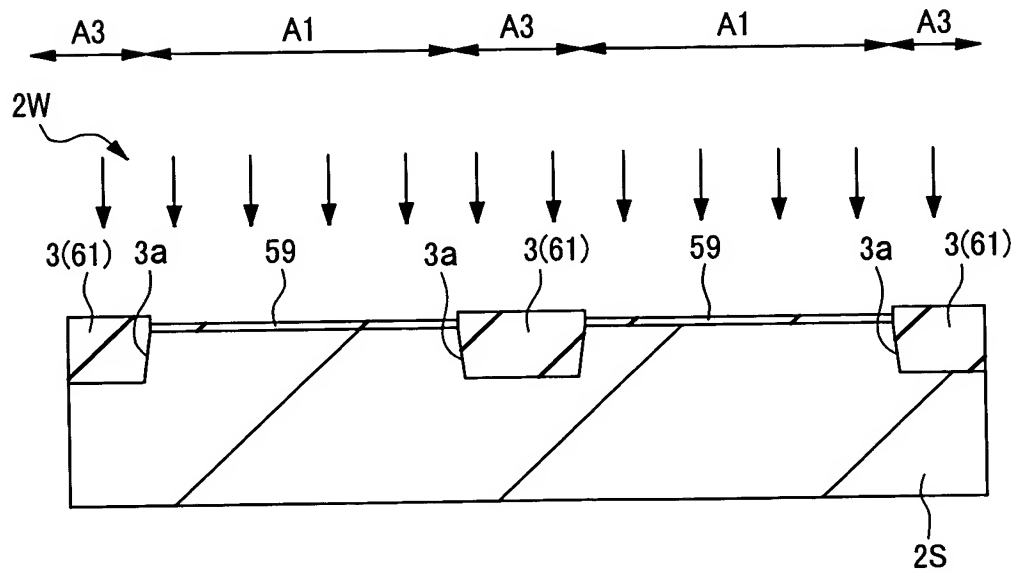


FIG.59

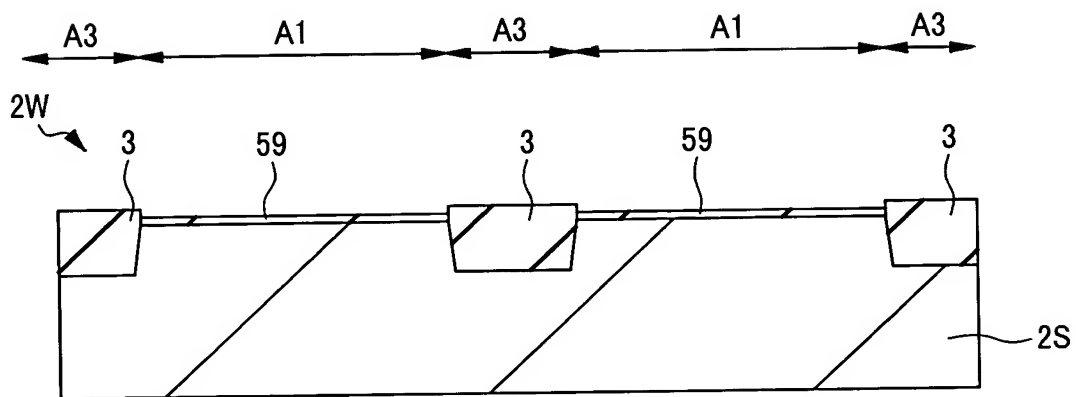


FIG.60

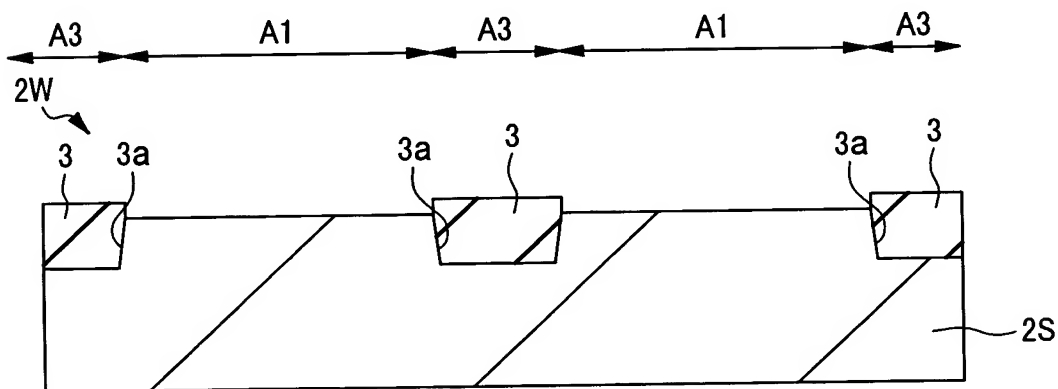
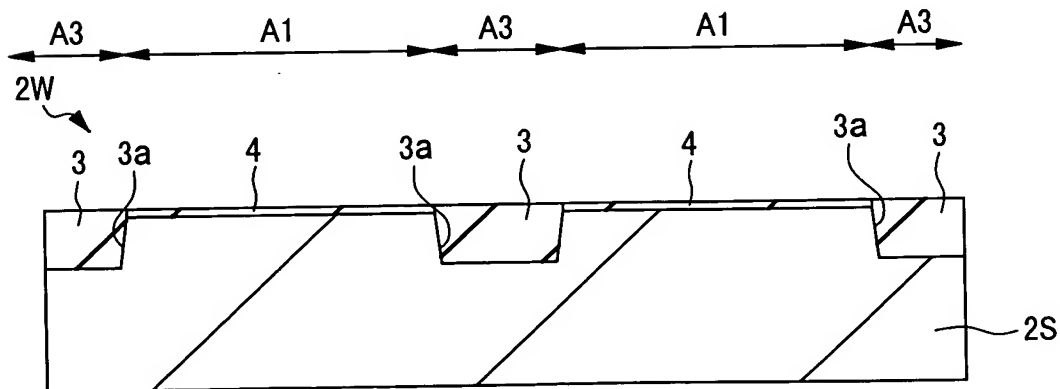
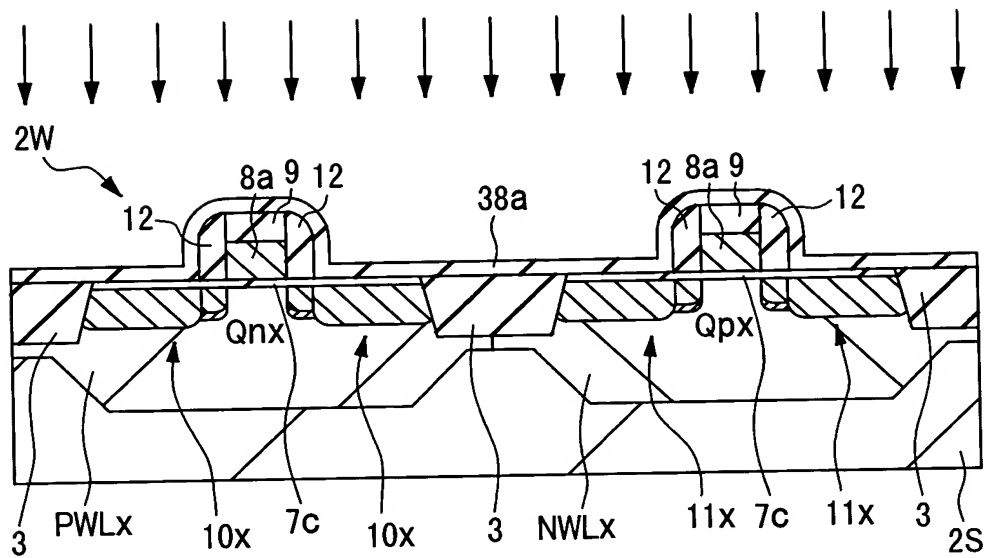


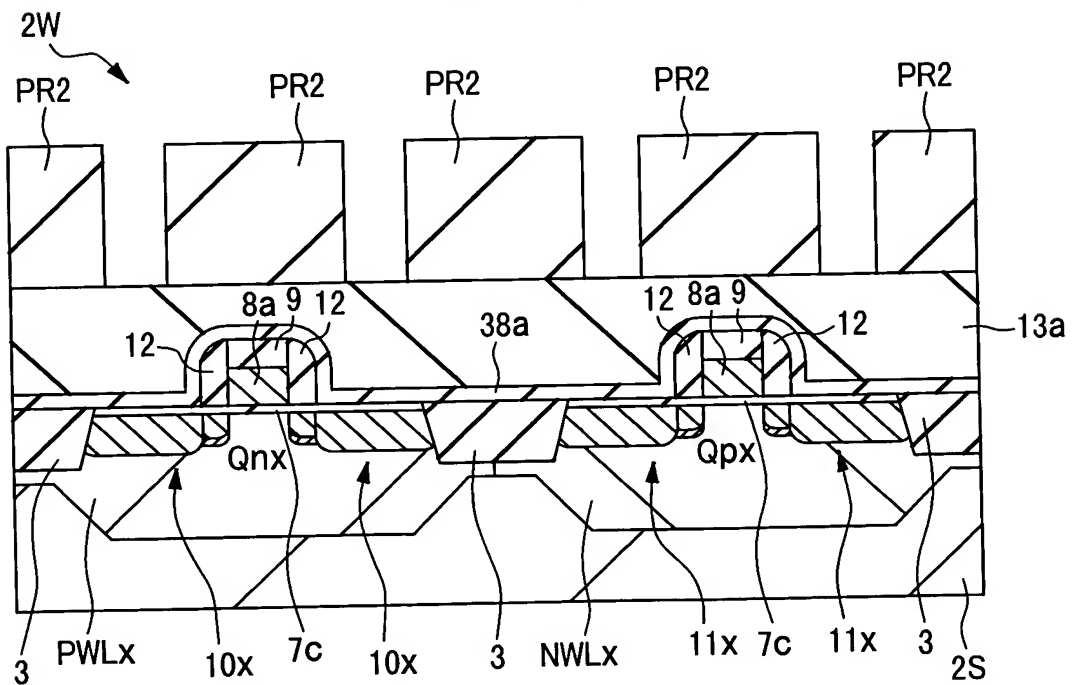
FIG.61



# FIG.62



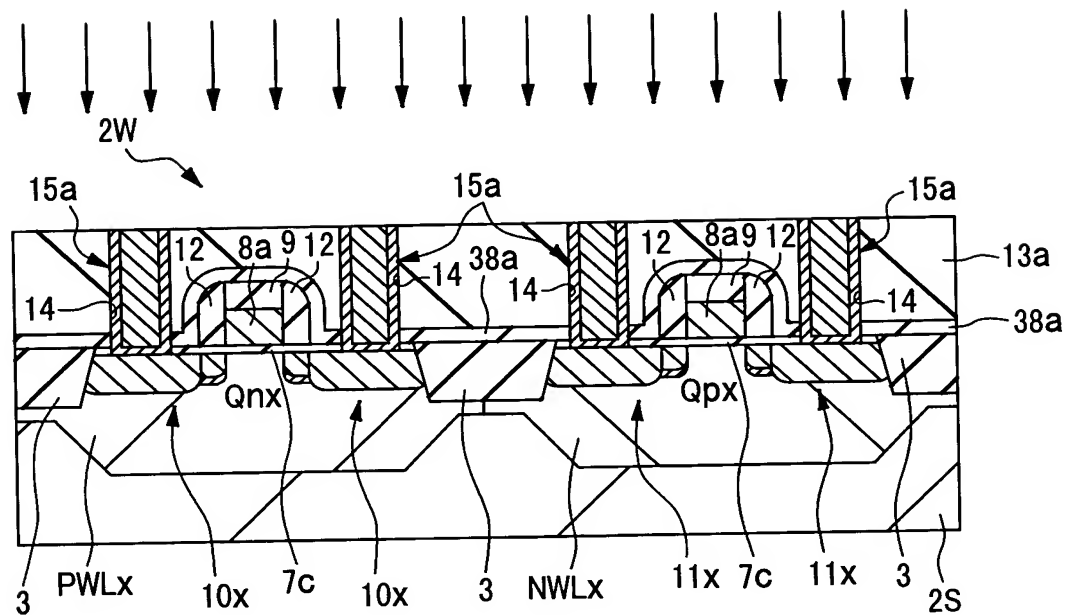
# FIG.63



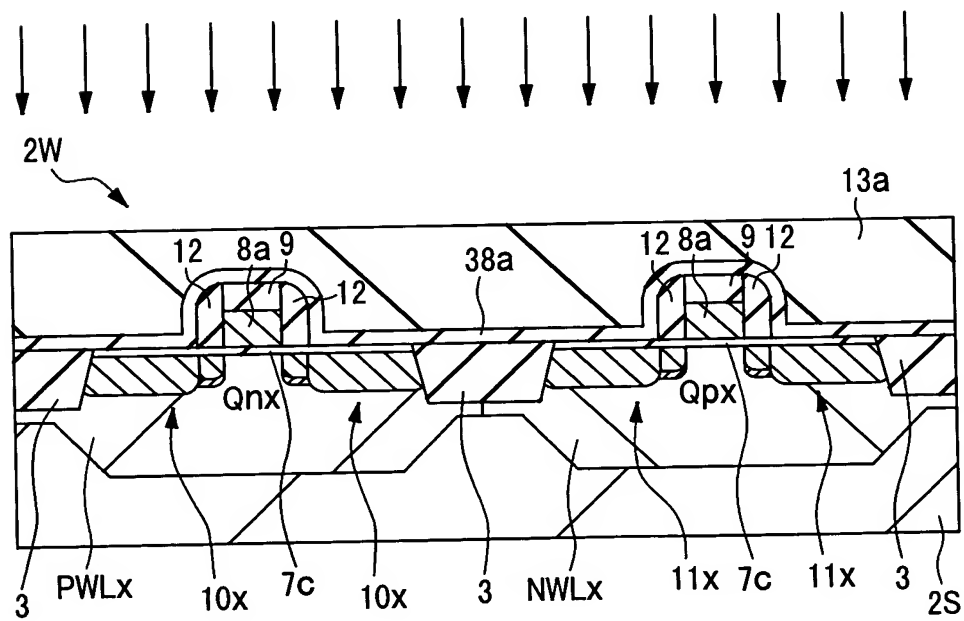
[illegible]



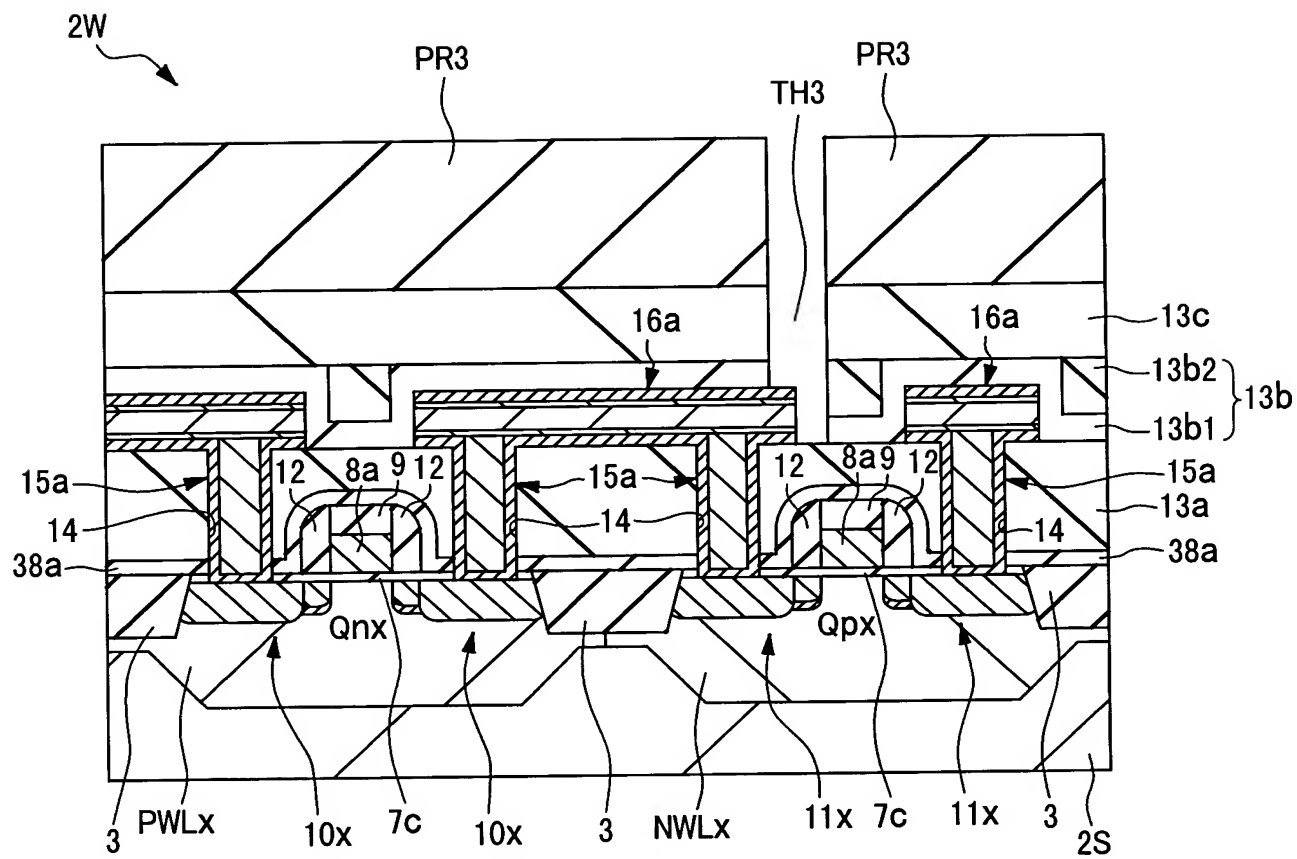
# FIG.66



# FIG.67



# FIG.68



# FIG.69

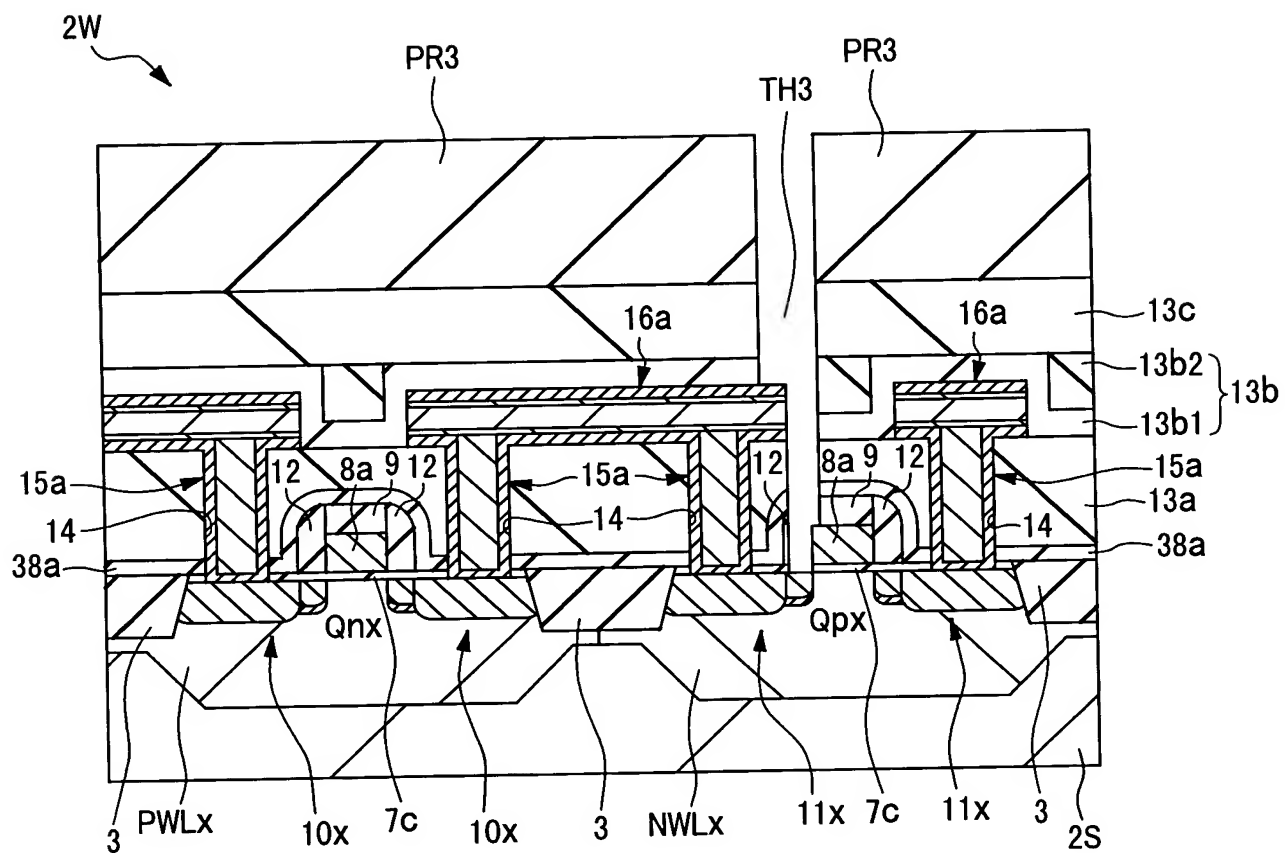


FIG.70

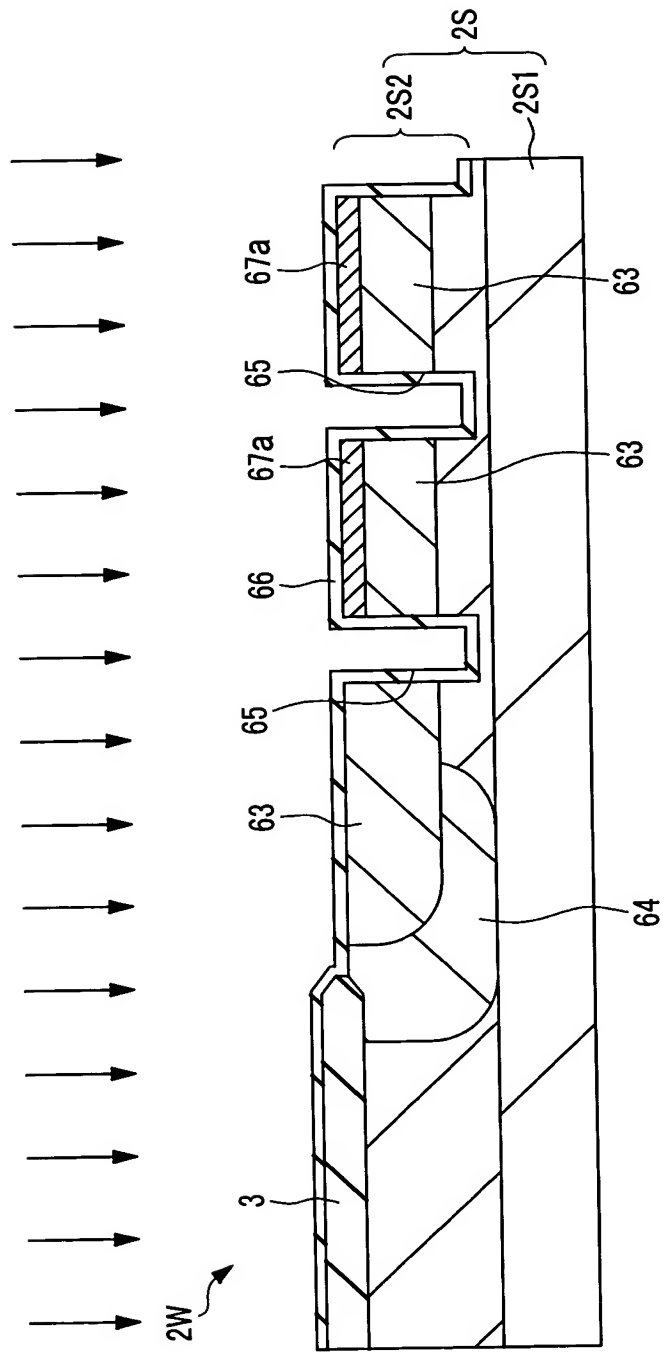
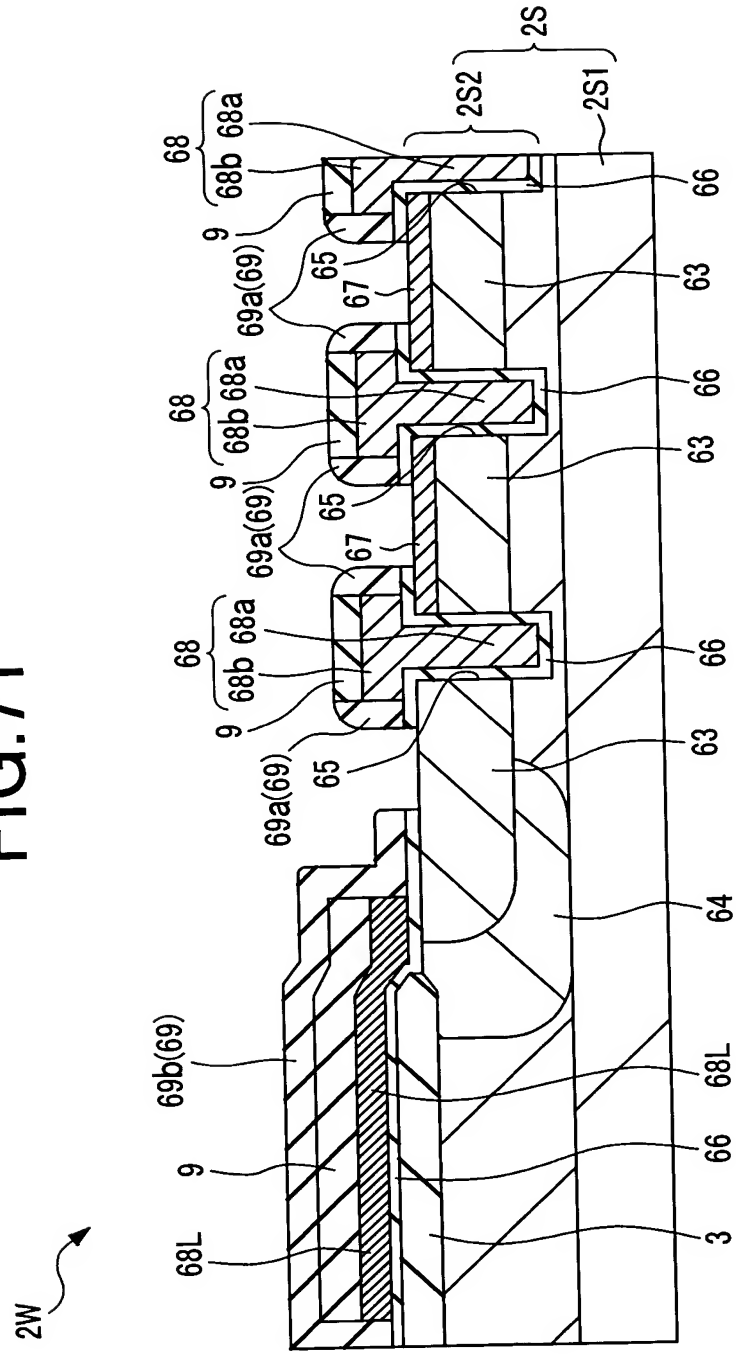


FIG. 71



**FIG. 72**

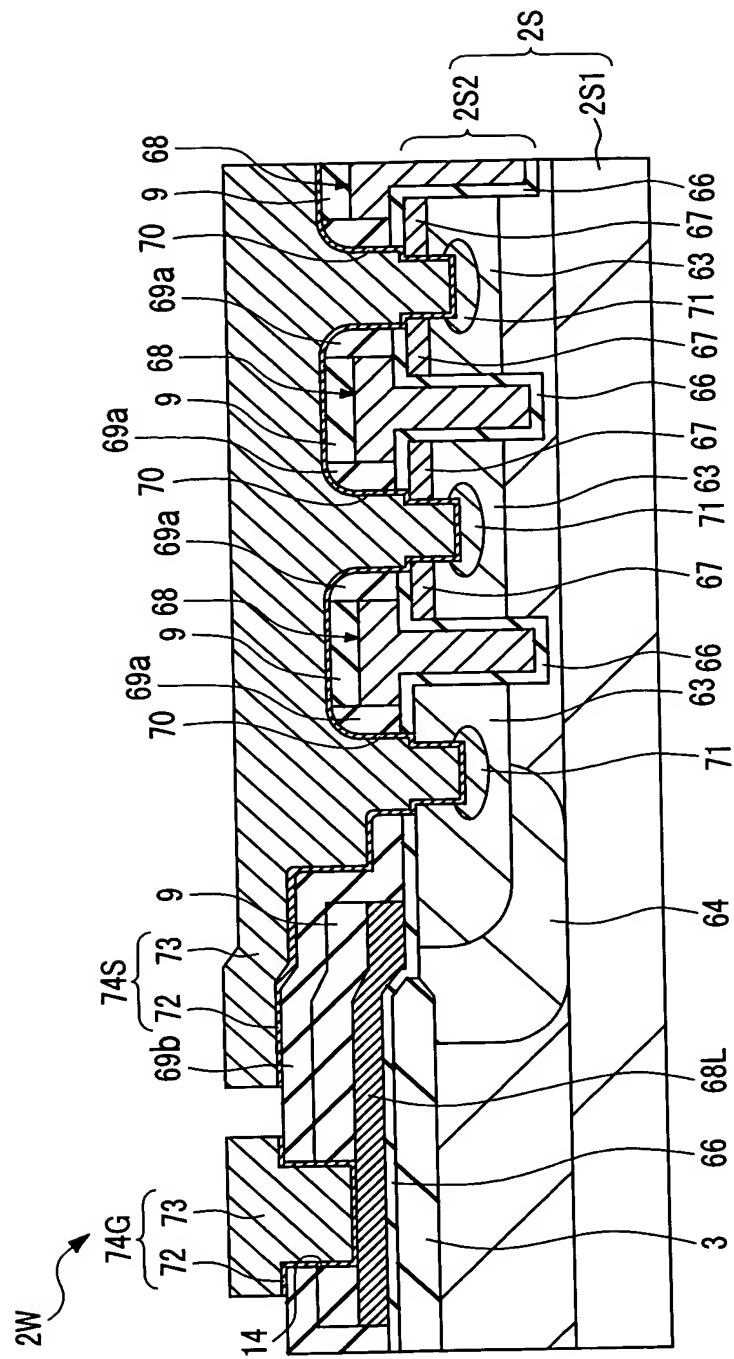


FIG.73

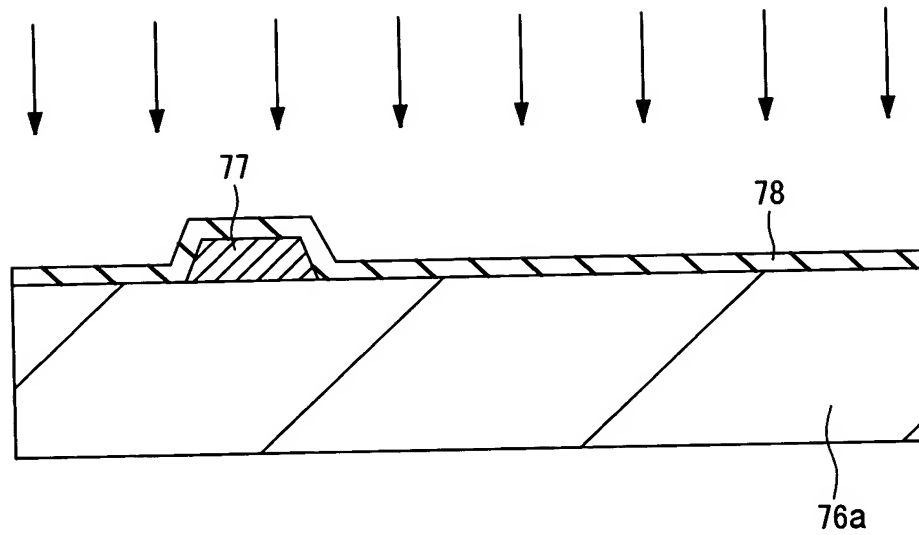


FIG.74

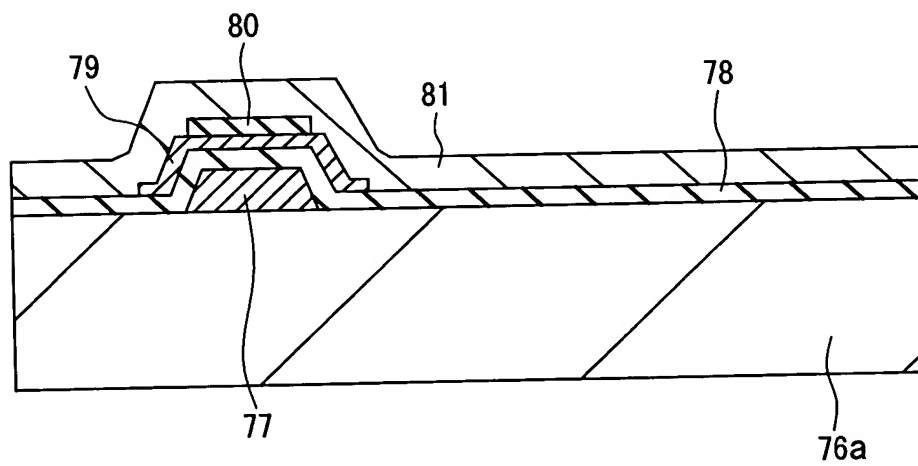


FIG.75

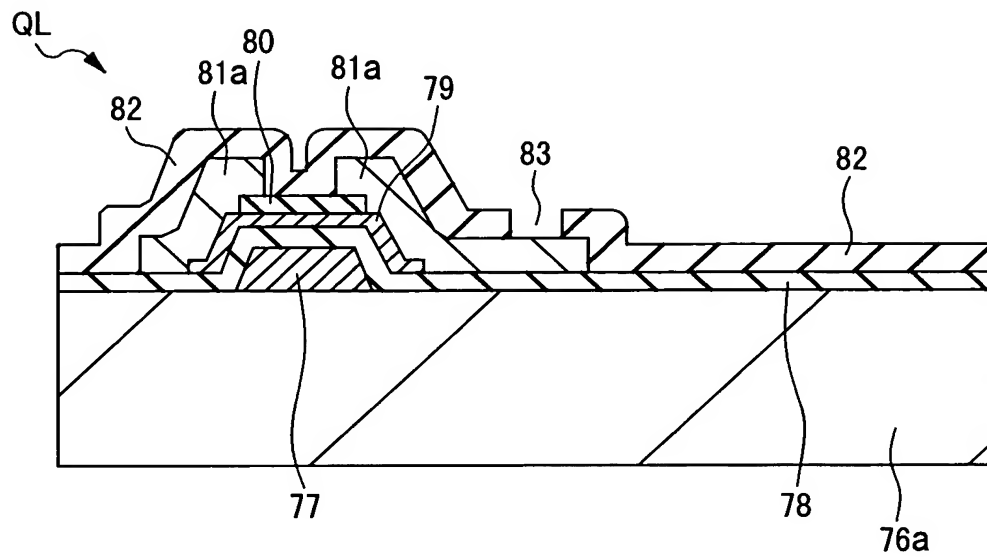


FIG.76

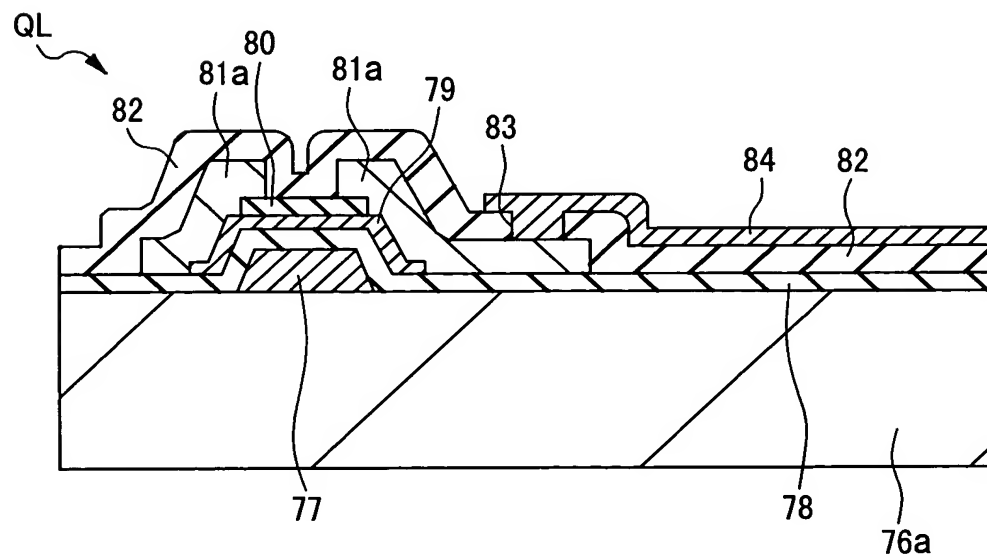




FIG.77

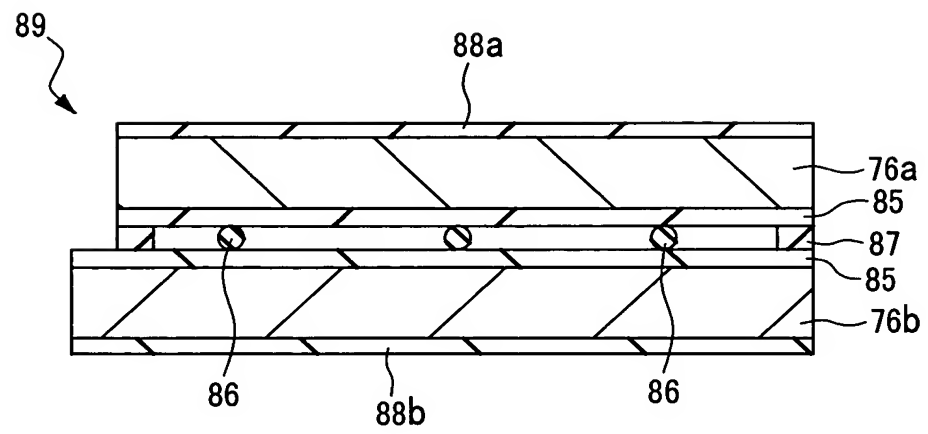


FIG.78

